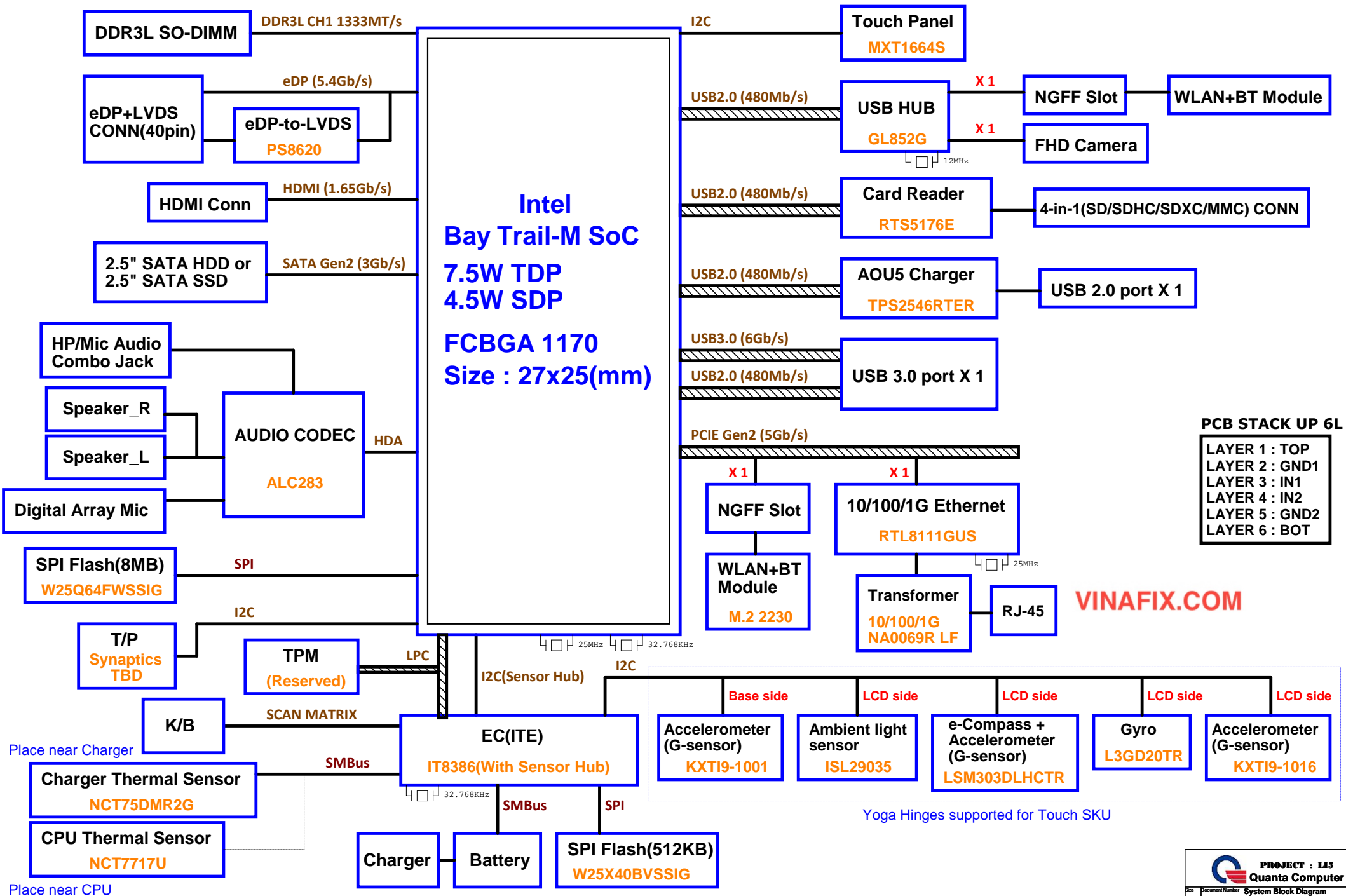
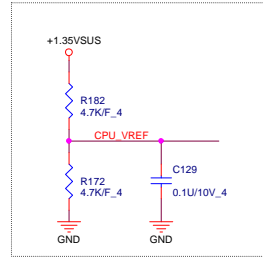
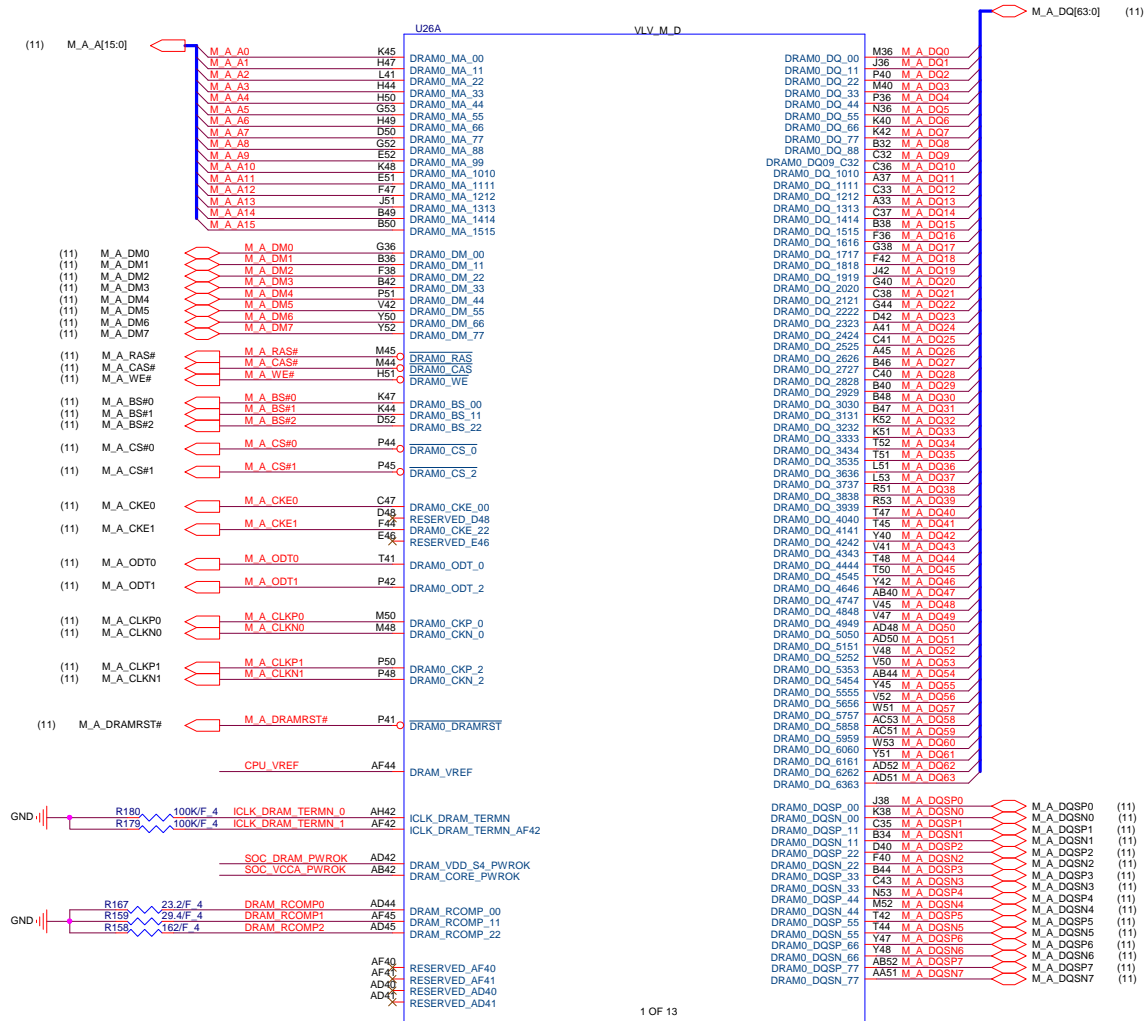


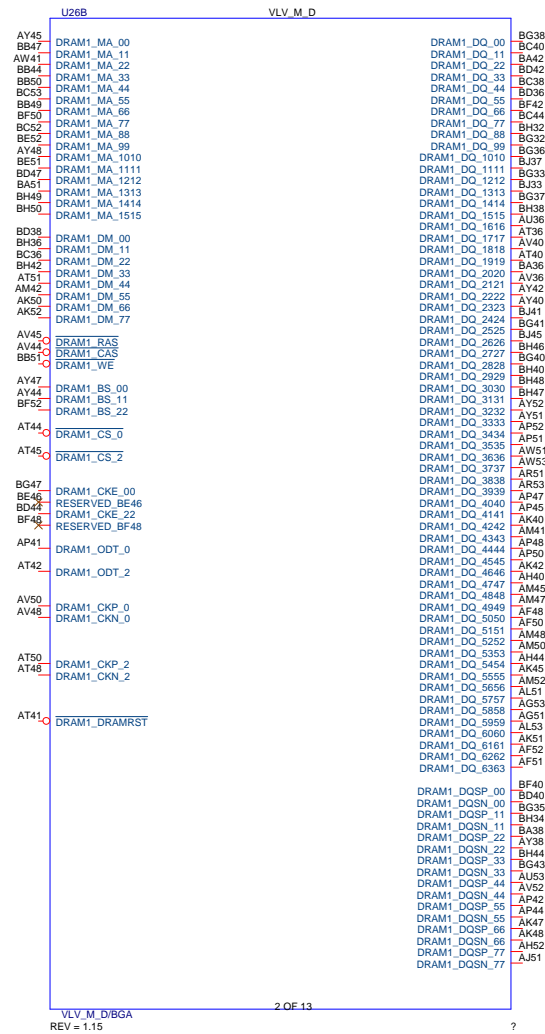
MR01 Intel Bay Trail-M Platform UMA Block Diagram (Windows)

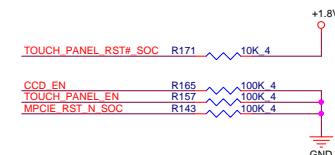
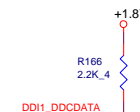


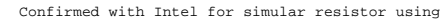


Note: PLACE TWO 4.7K RESISTORS
CLOSE TO CPU PINS ON M_VREF

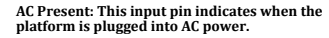


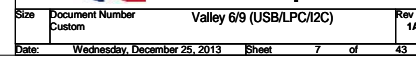


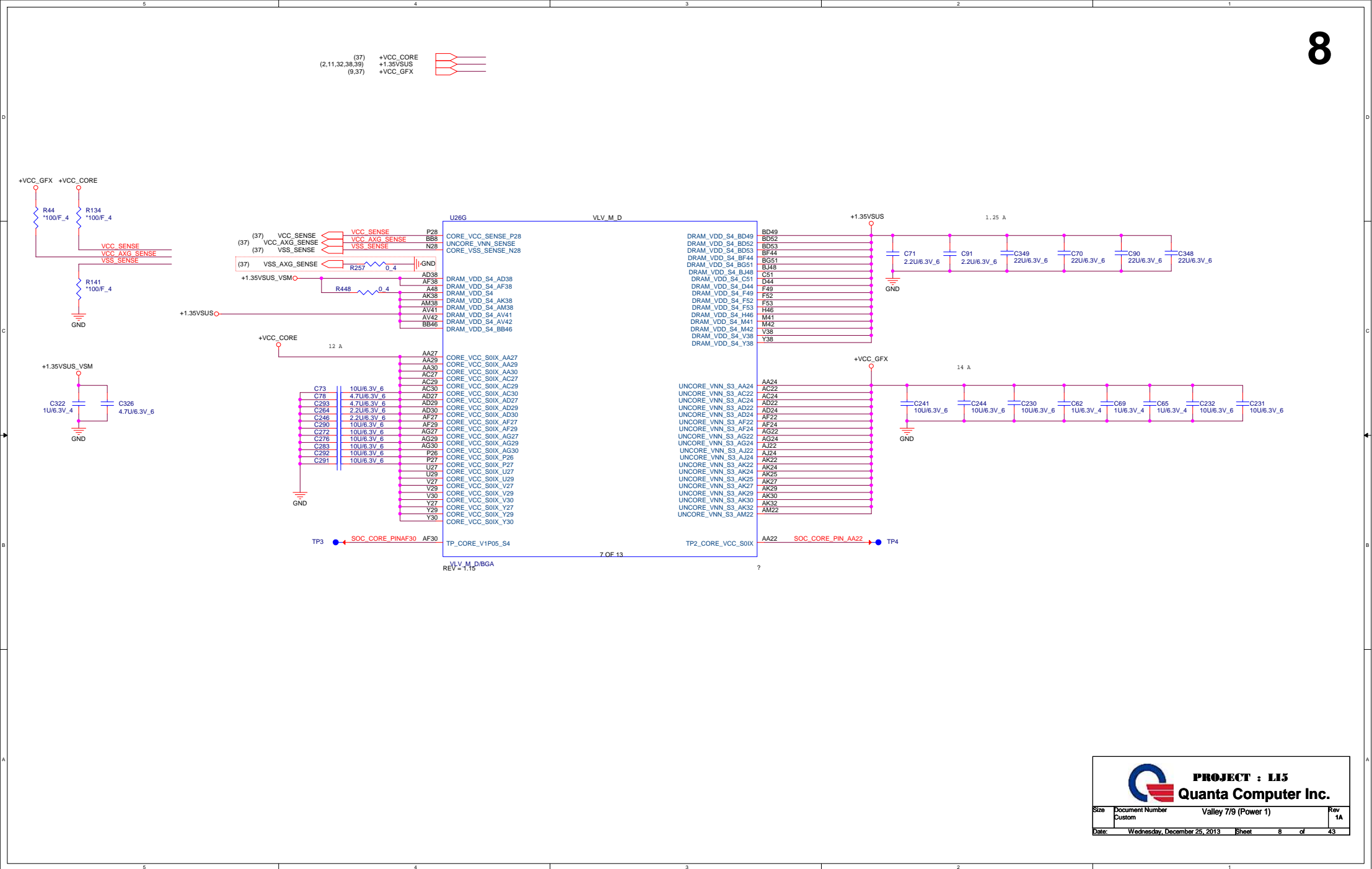


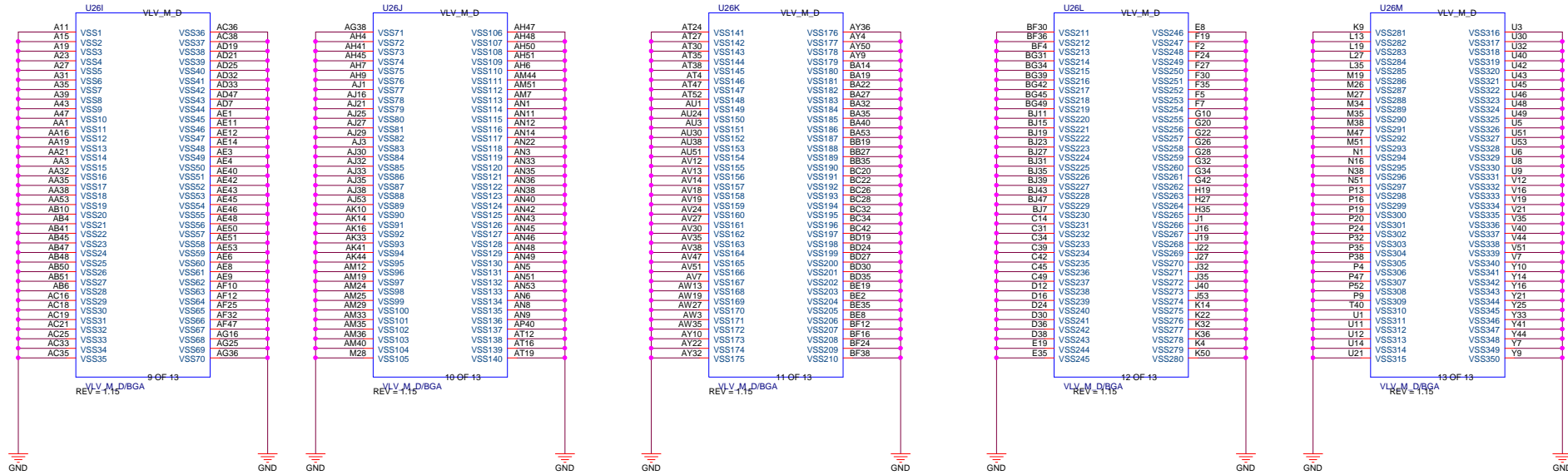


SKU	BID2	BID1	BID0
Non-Touch	X	X	0
Touch	X	X	1
I2C TP	X	0	X
PS2 TP	X	1	X







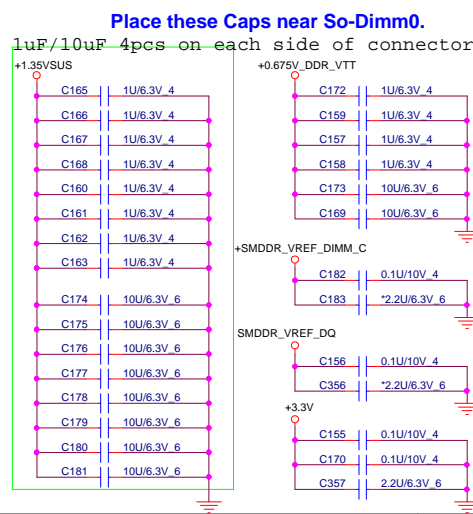
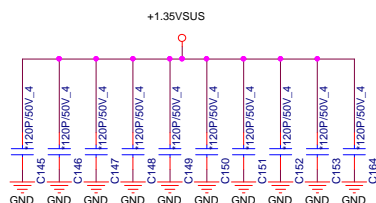
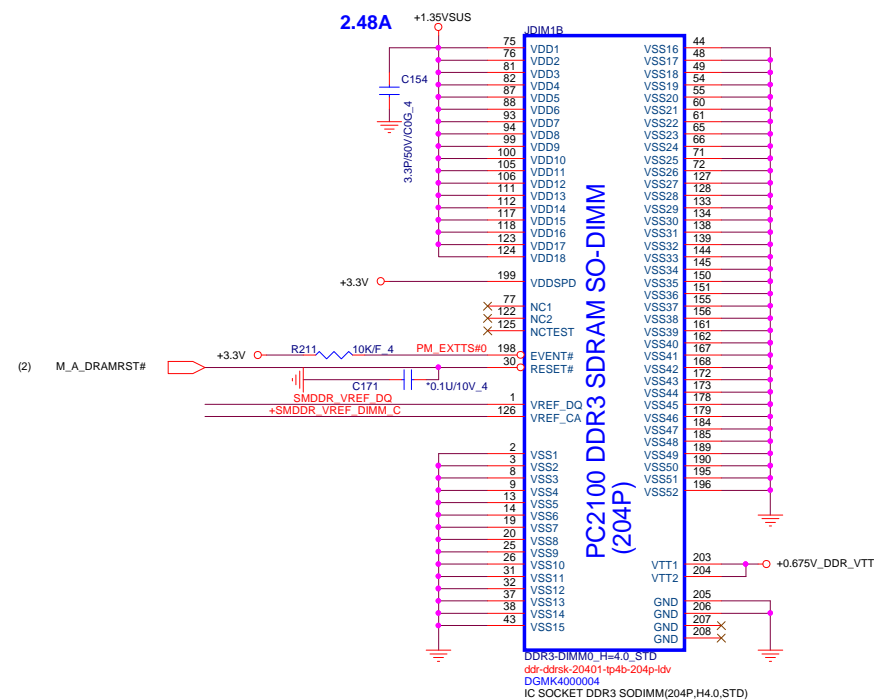
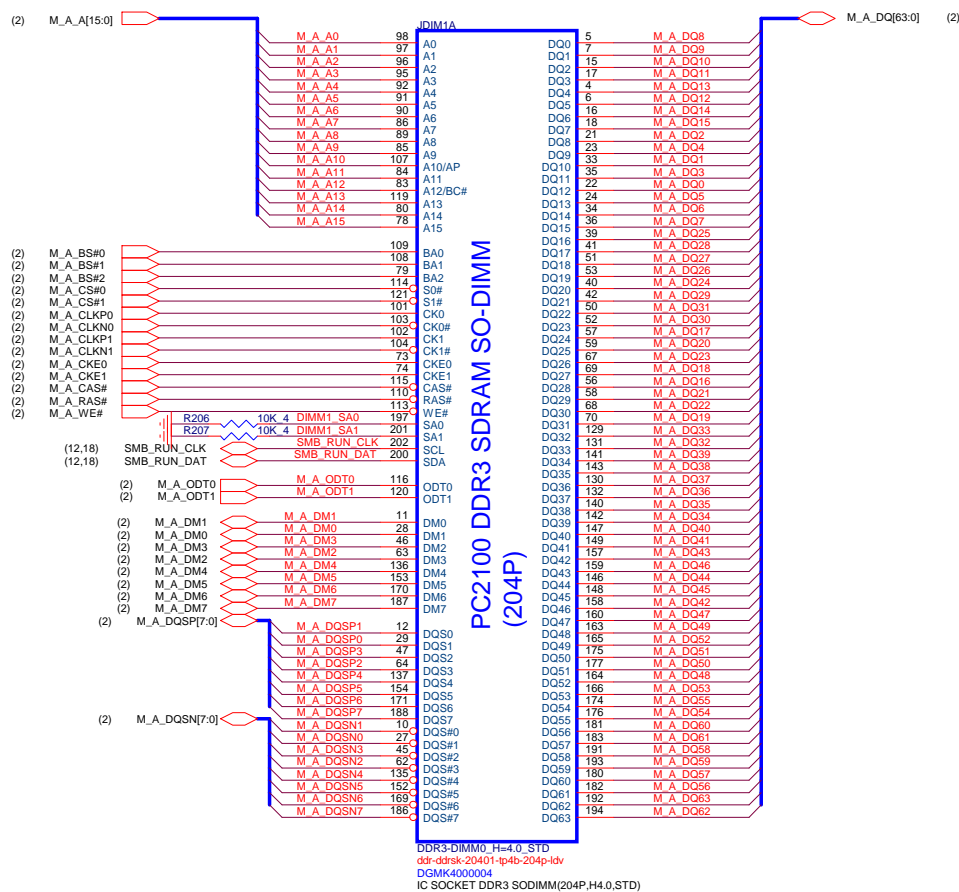


VINAFIX.COM

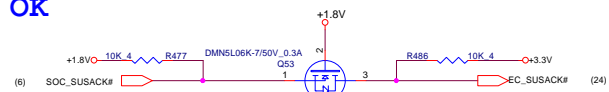


PROJECT : LI5
Quanta Computer Inc.

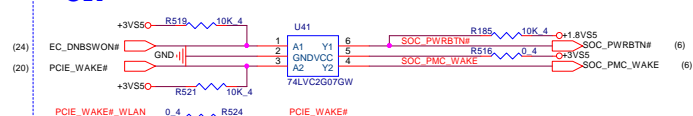
Size	Document Number	Valley 9/9 (GND)	Rev
	Custom		1A
Date:	Wednesday, December 25, 2013	Sheet 10 of 43	



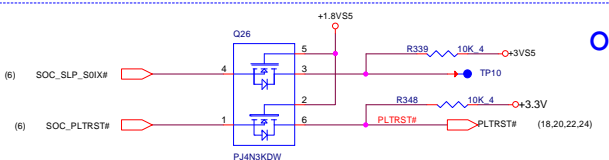
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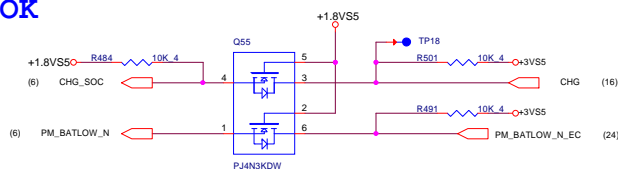
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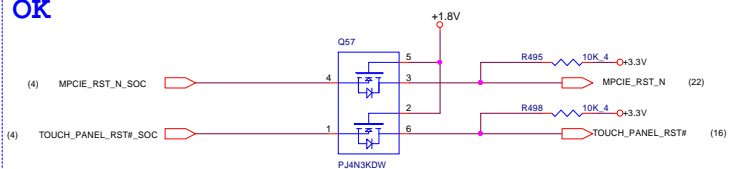
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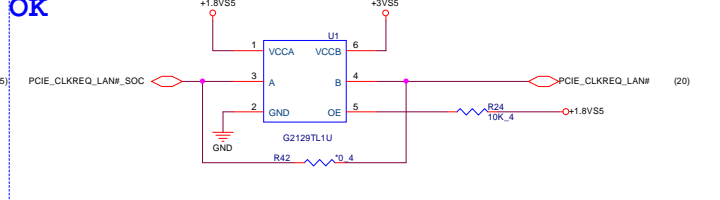
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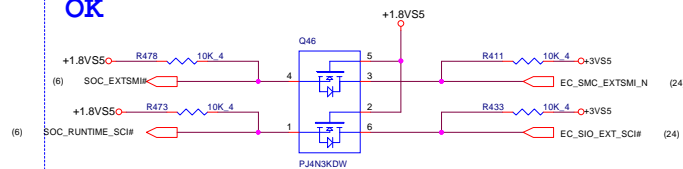
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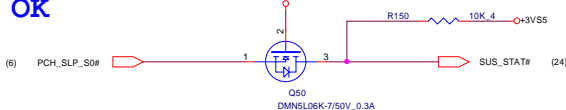
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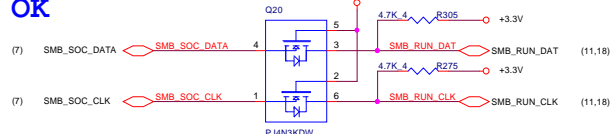
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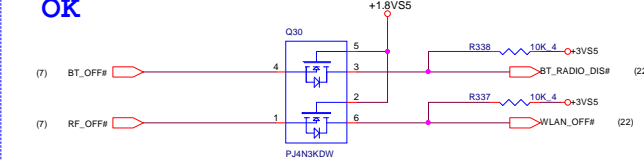
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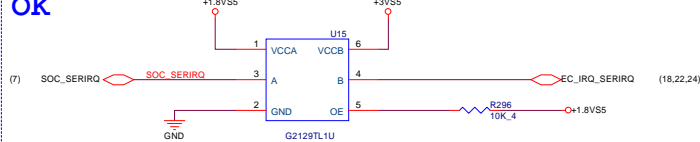
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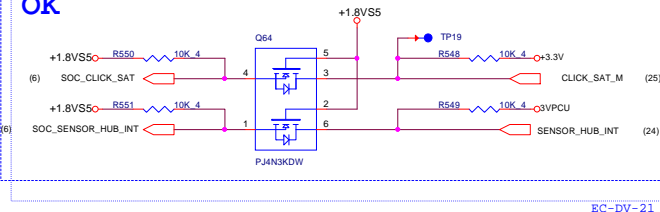
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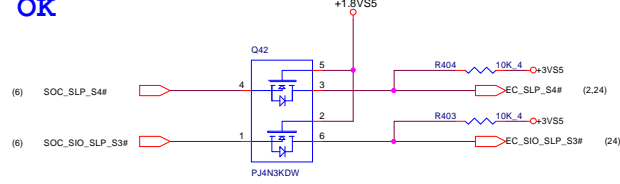
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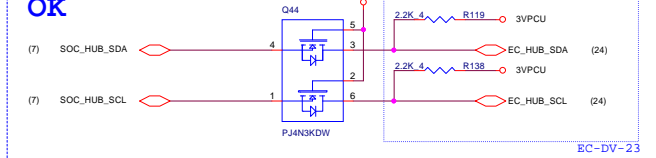
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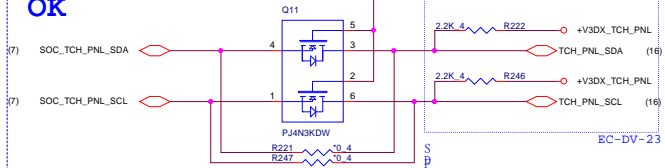
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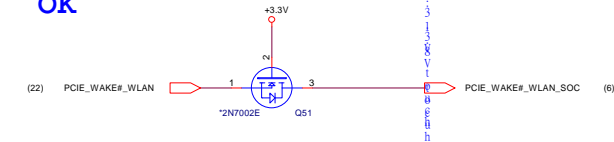
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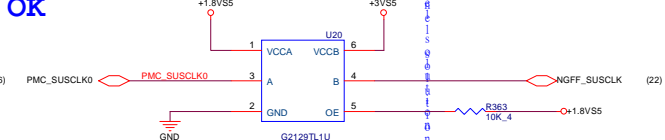
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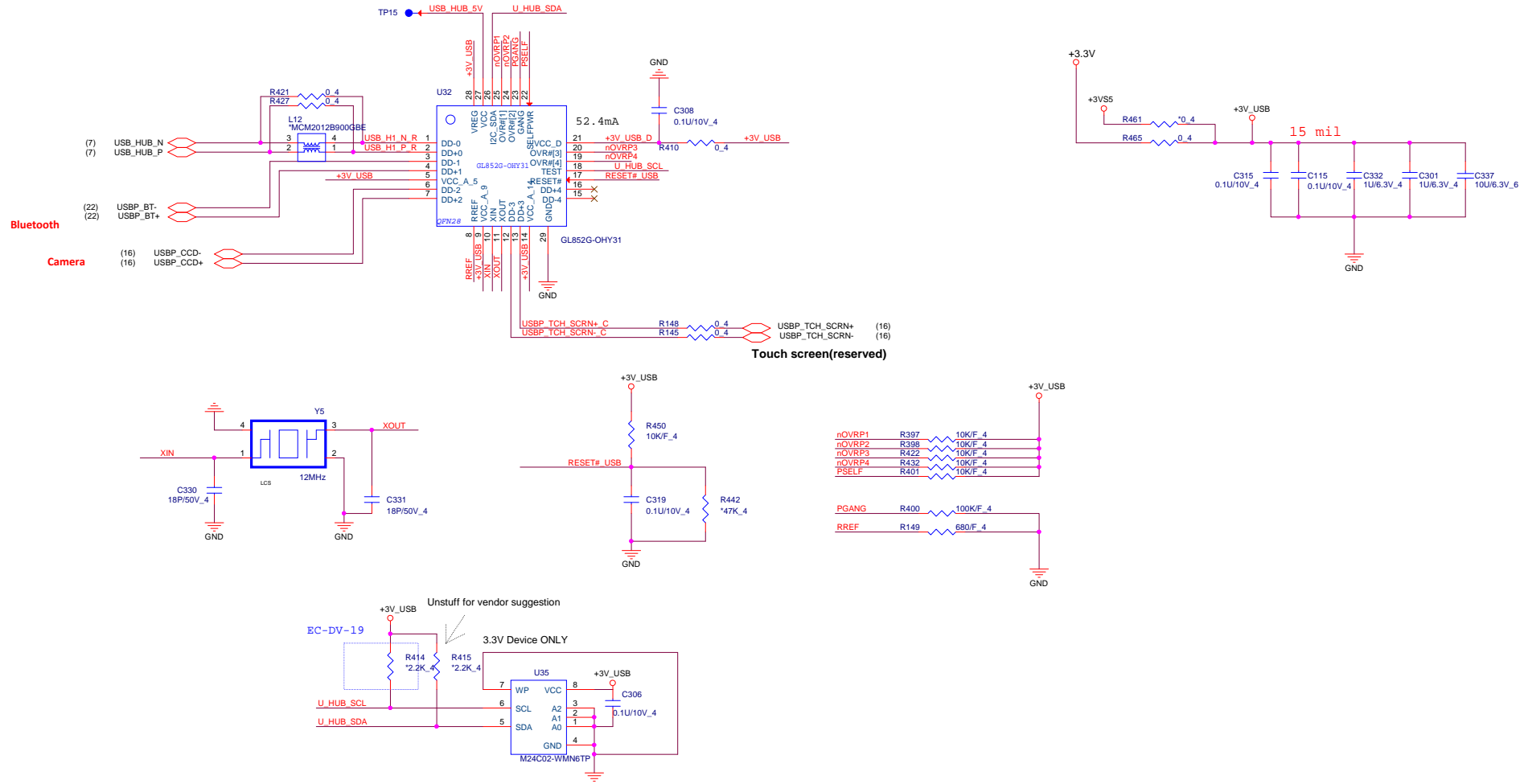


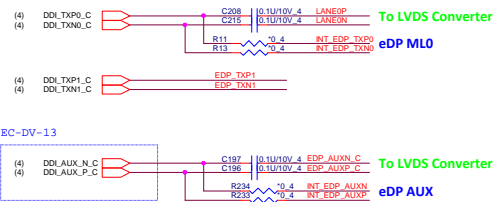
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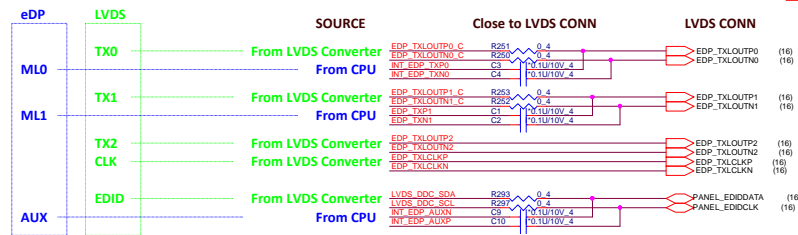
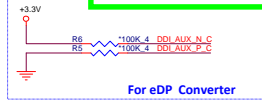
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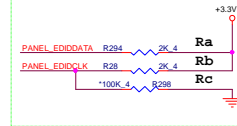




For eDP Only: stuff Resistor
For LVDS only stuff Cap

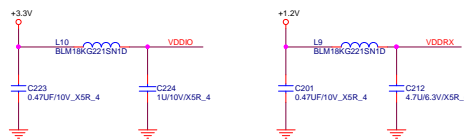
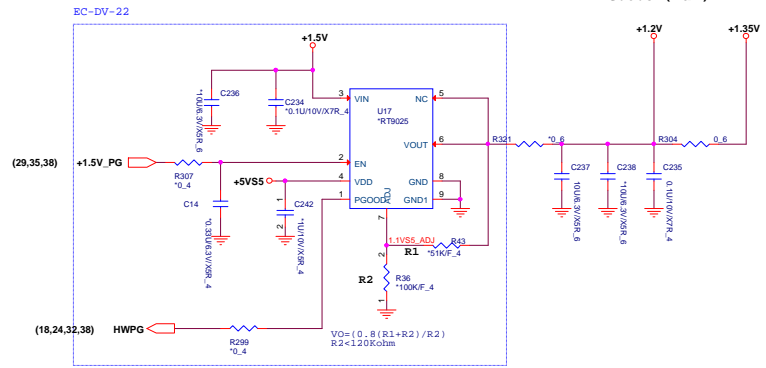


LVDS mode only

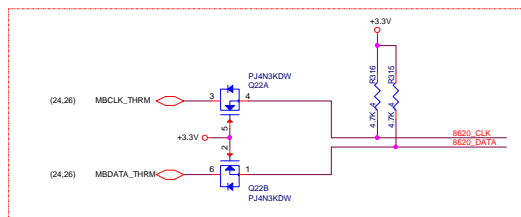
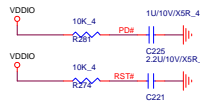


1.2V Power Source

1.2 V(1.35V) +/- 5%
TDC: 0.3A(max)

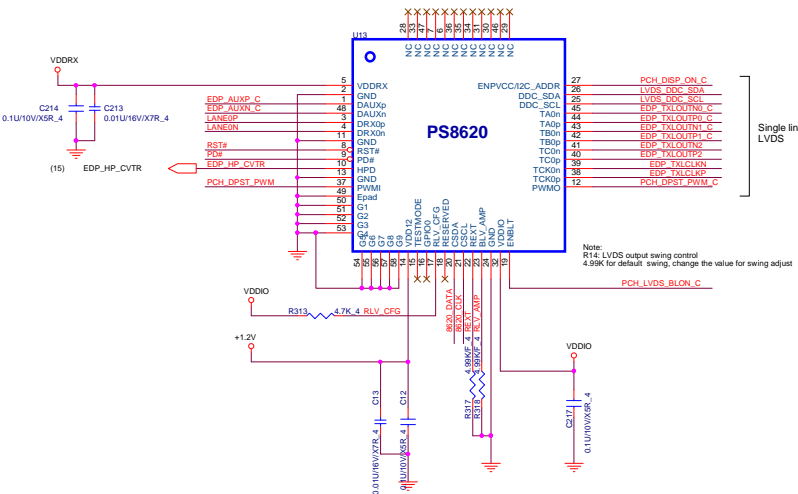
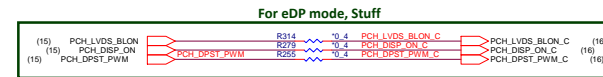


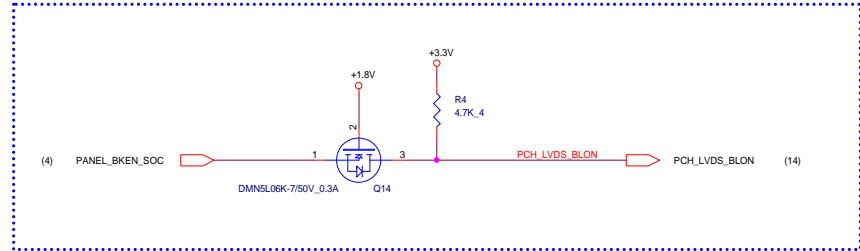
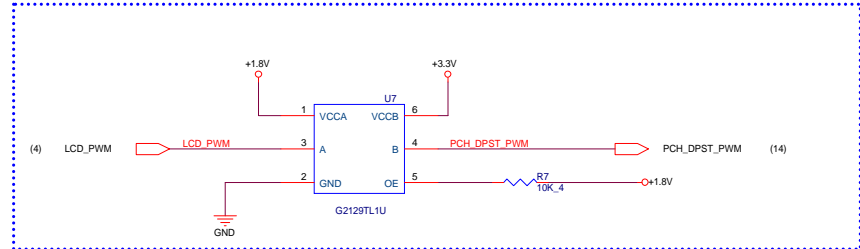
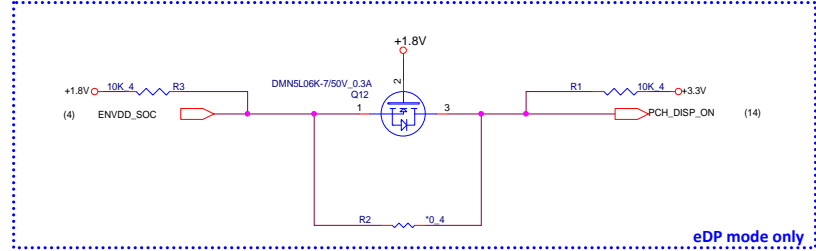
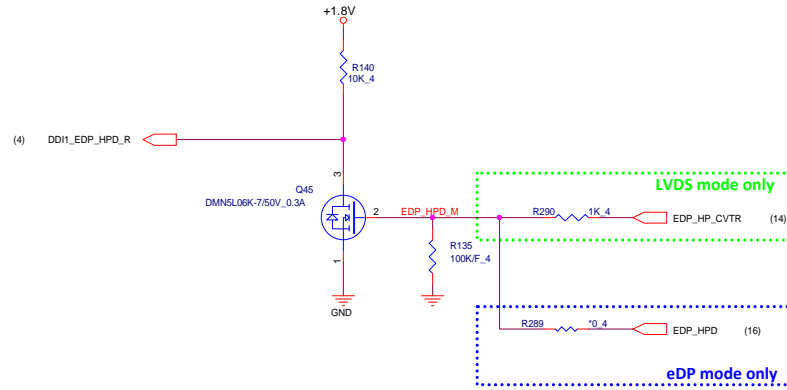
Power On Configuration



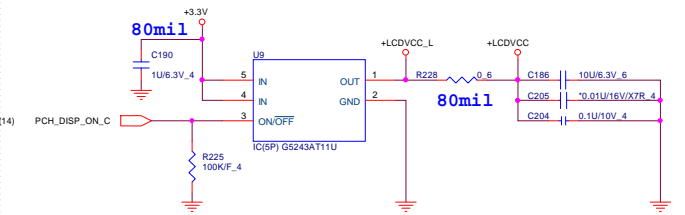
Switching Regulator Layout Guideline

- Place the switching regulator inductor (L3) close to SW_OUT Pin (Pin13).
- The SW_OUT output traces should be as wide as possible.
- The GNDX pin (Pin14) should be connected to the main PCB ground plane, with the device GND pin of the PS8622 connected to separate GND island (GND4) for the device. The GND island (GND4) should be connected to the main GND plane (GND) with a single-point connection by use of a wide PCB trace.
- Place the 4.7uF decoupling Capacitor (C4) for VDDIOX close to VDDIOX pin.
- The GND of the 4.7uF capacitor (C4) for VDDIOX should be placed close to the GND of 4.7uF capacitor (C3) behind inductor.
- Place the bead (L2) for VDDIOX close to PS8622.

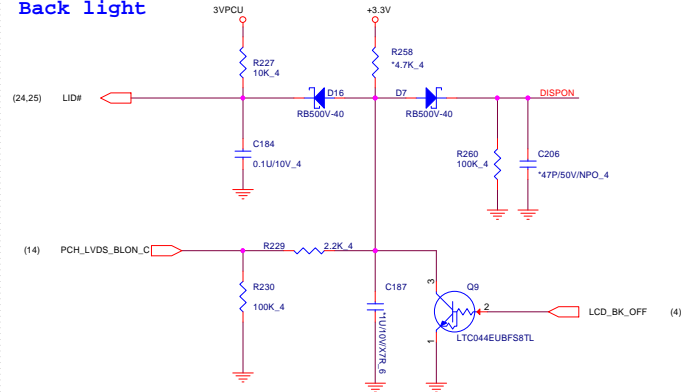




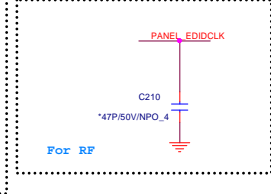
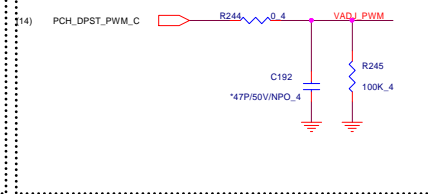
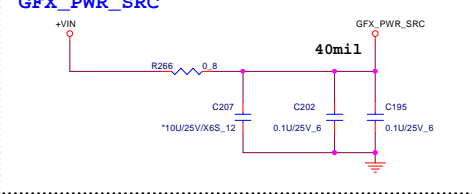
LCDVCC



Back light

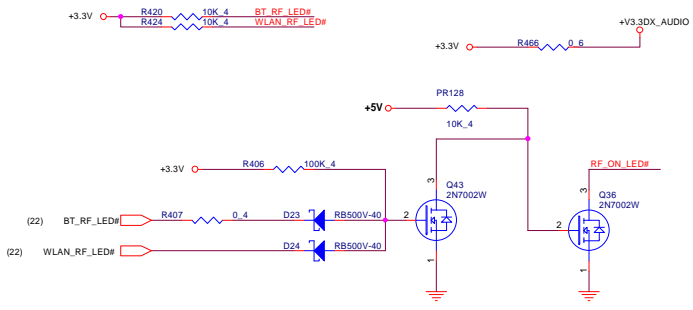
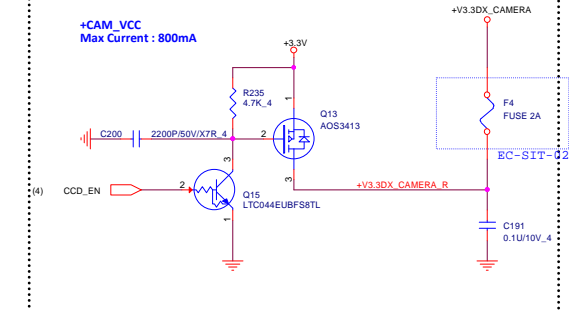


GFX_PWR_SRC

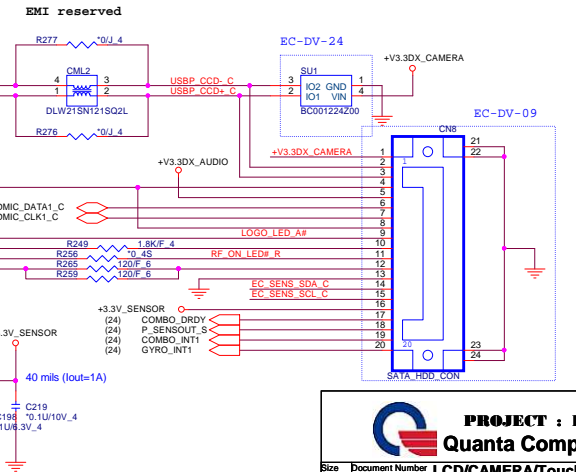
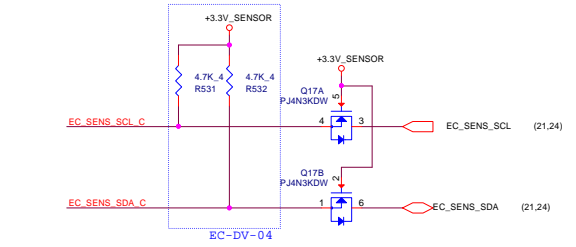
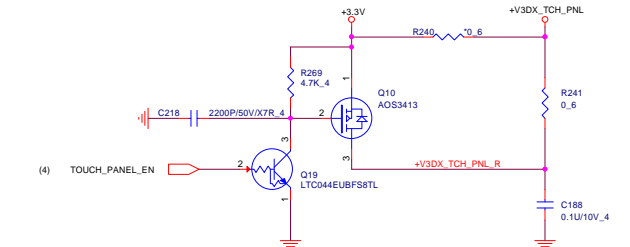


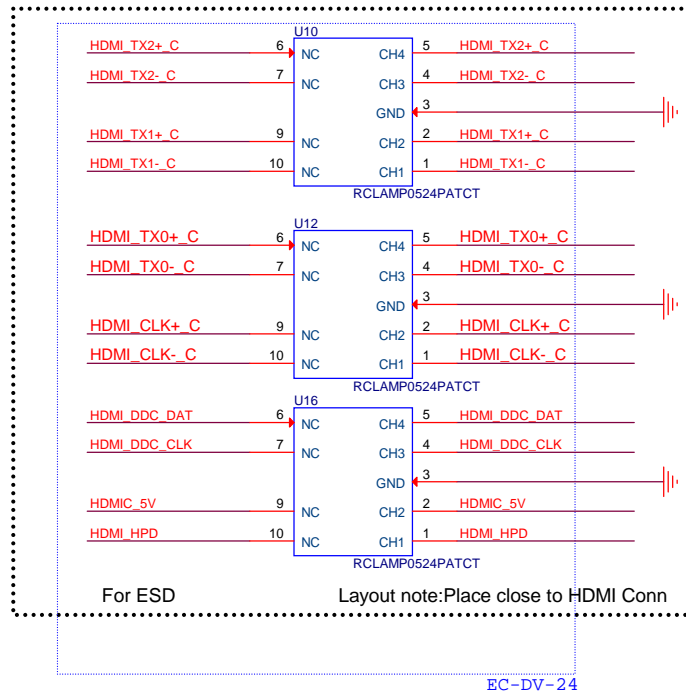
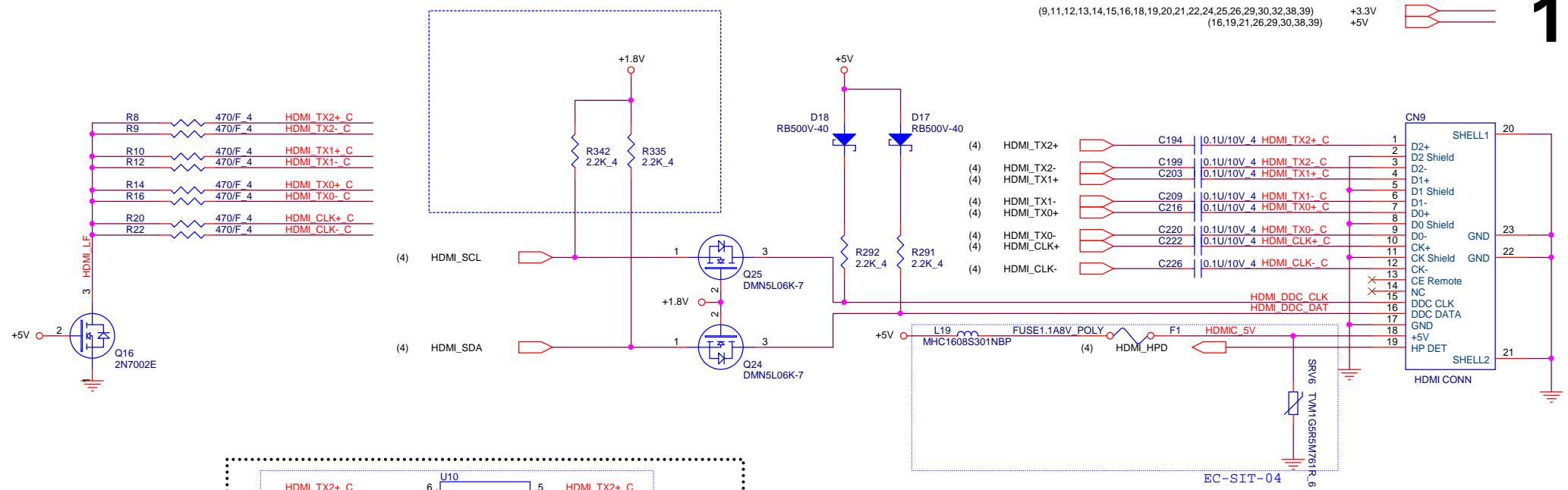
CCD+MIC+LOGO+WLAN LED CONN

CAMERA VCC Control

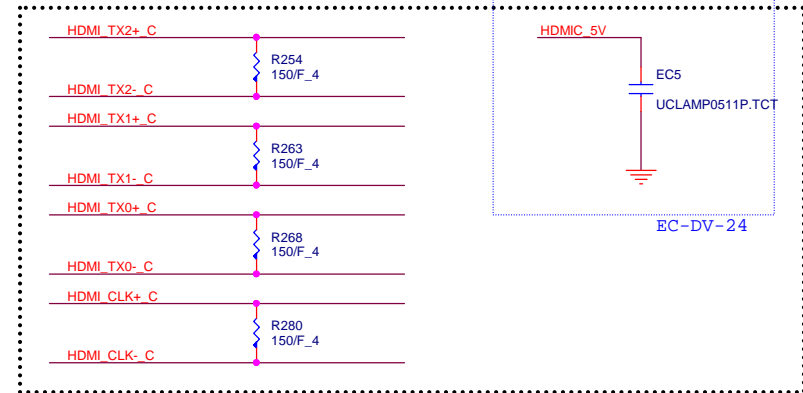


Touch Panel VCC Control

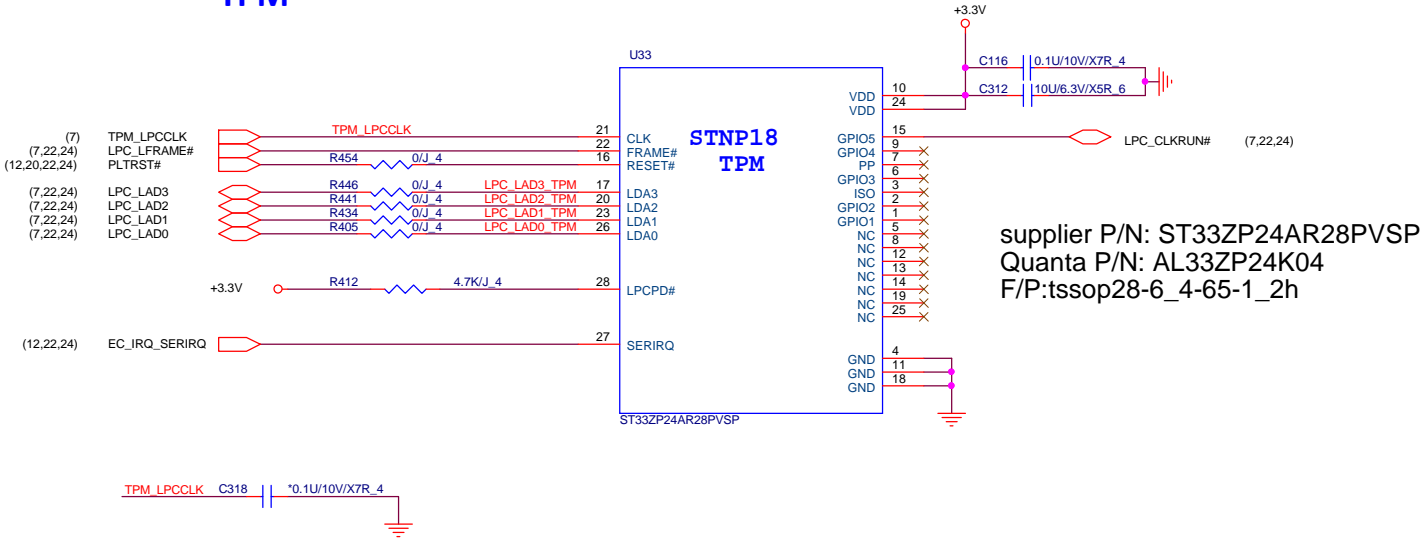




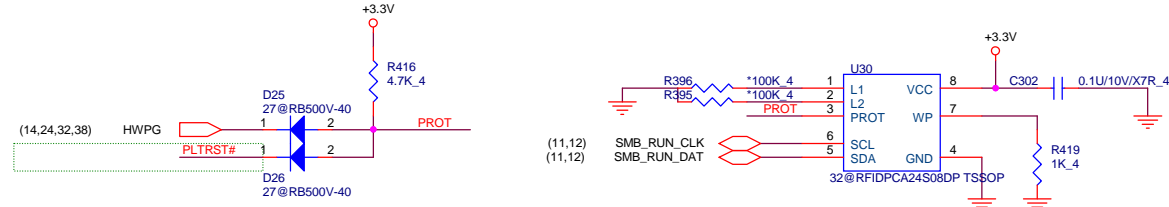
EMI reserve for HDMI



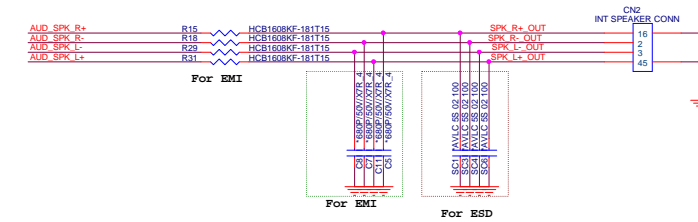
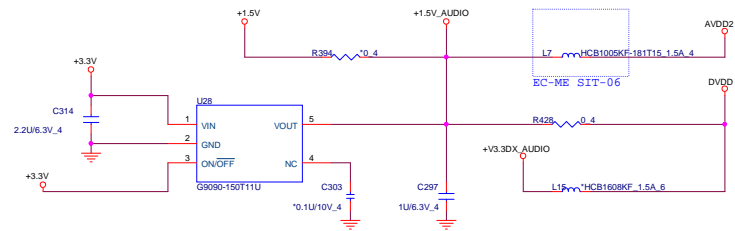
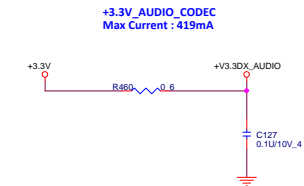
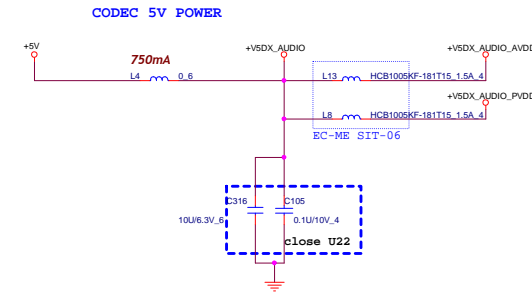
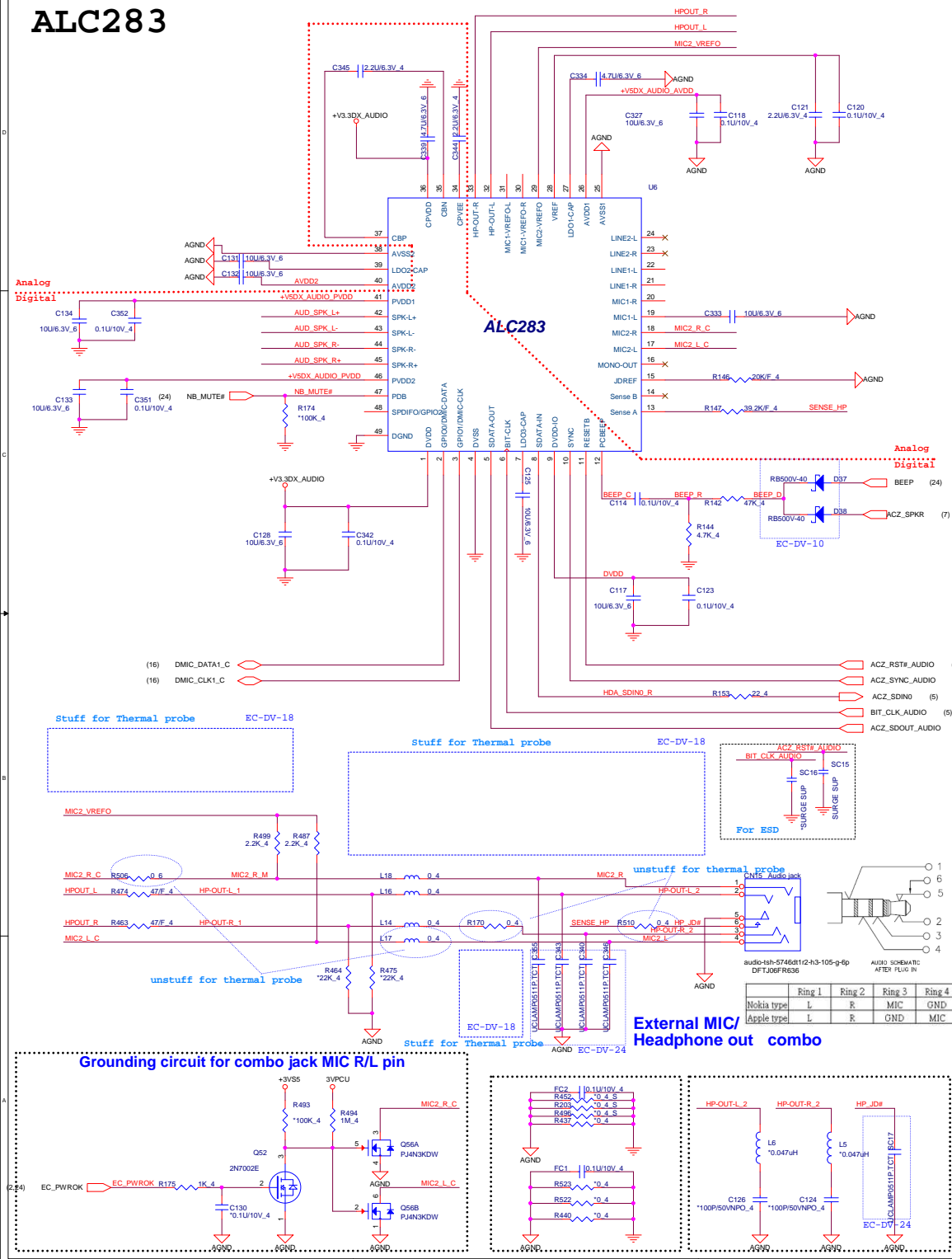
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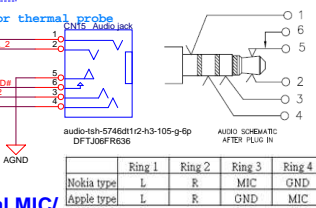
RFID



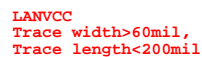
ALC283



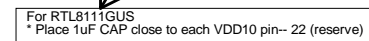
INT Speaker



	Ring 1	Ring 2	Ring 3	Ring 4
Nokia type	L	R	MIC	GND
Apple type	L	R	GND	MIC

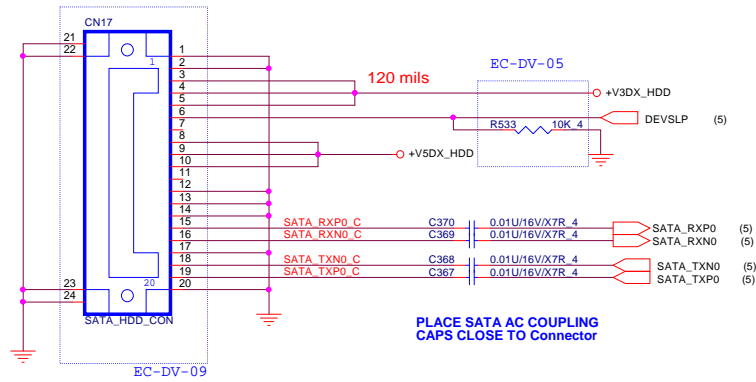


10/100	RTL8106EUS-CG	AL008106002
1G	RTL8111GUS-CG	AL008111009

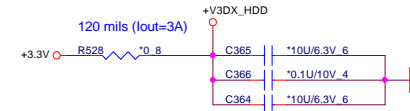


EC-ME SIT-03

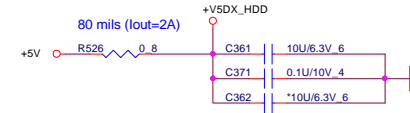




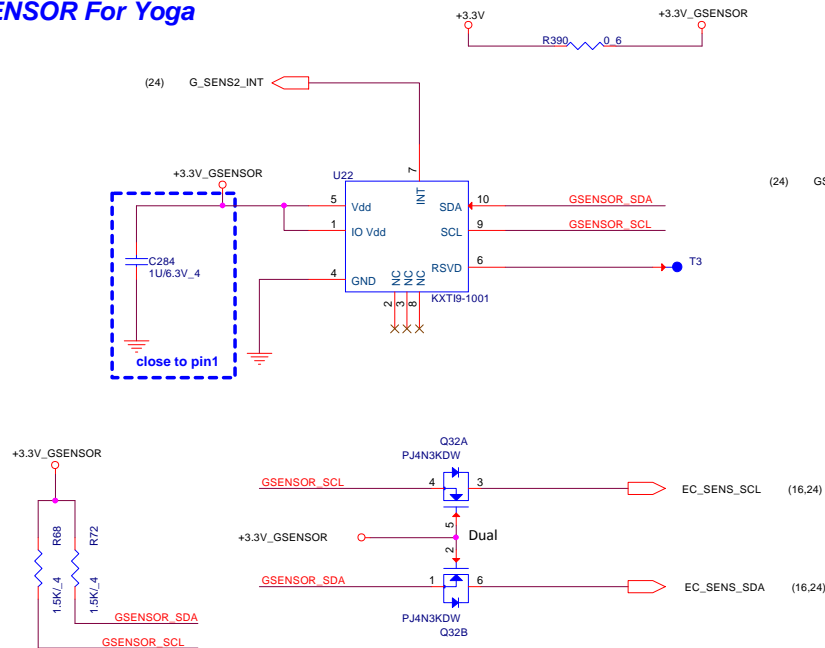
DC Current rating: 3 A (MAX)



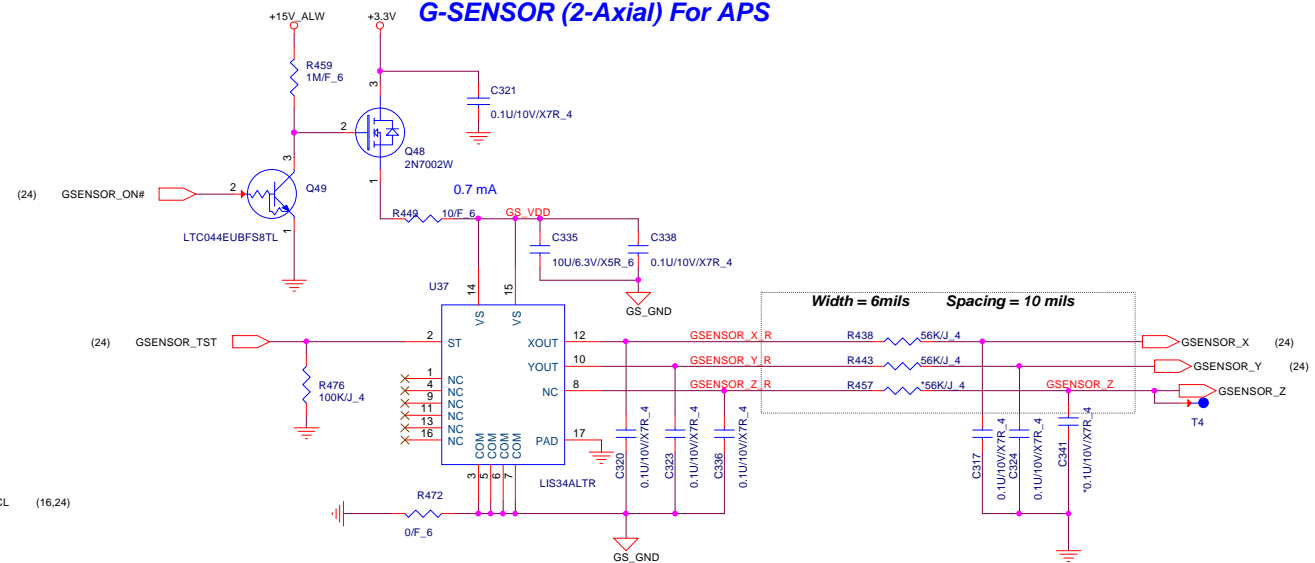
DC Current rating: 2 A (MAX)



G-SENSOR For Yoga



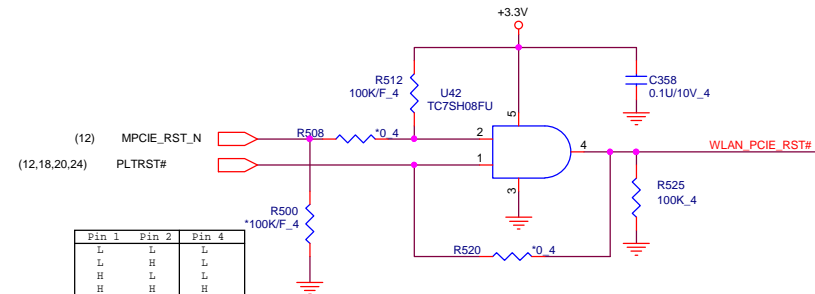
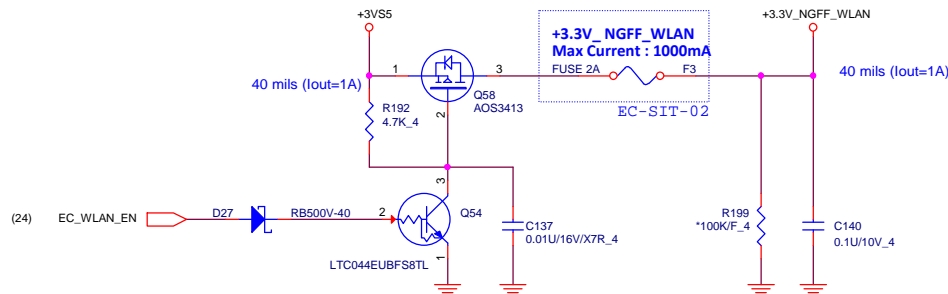
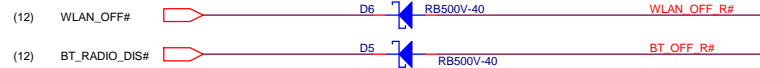
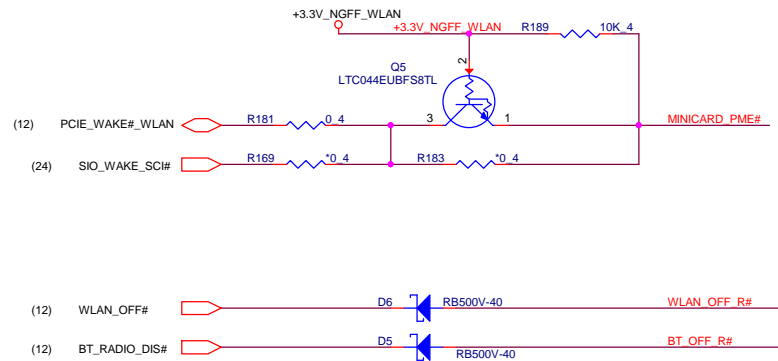
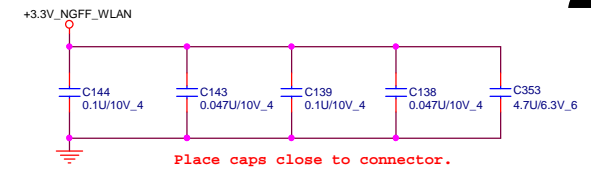
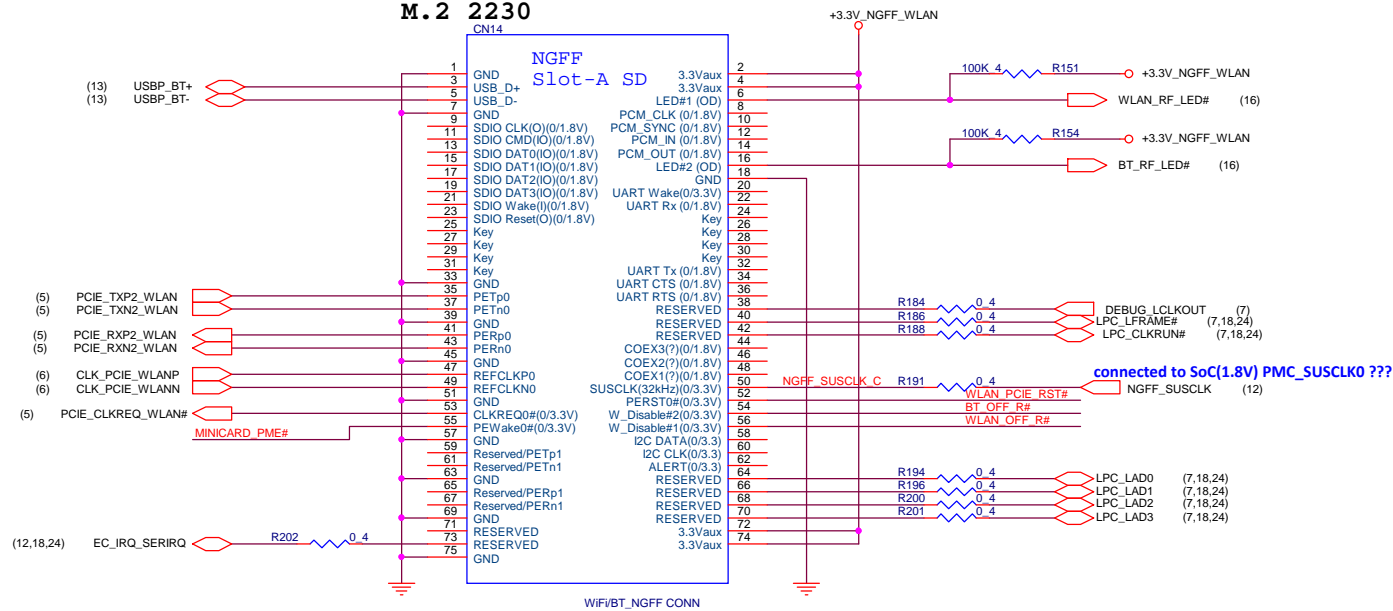
G-SENSOR (2-Axial) For APS



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Quanta Computer Inc.

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	SATA/G-sensor	1A
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NGFF WiFi/BT connector M.2 2230



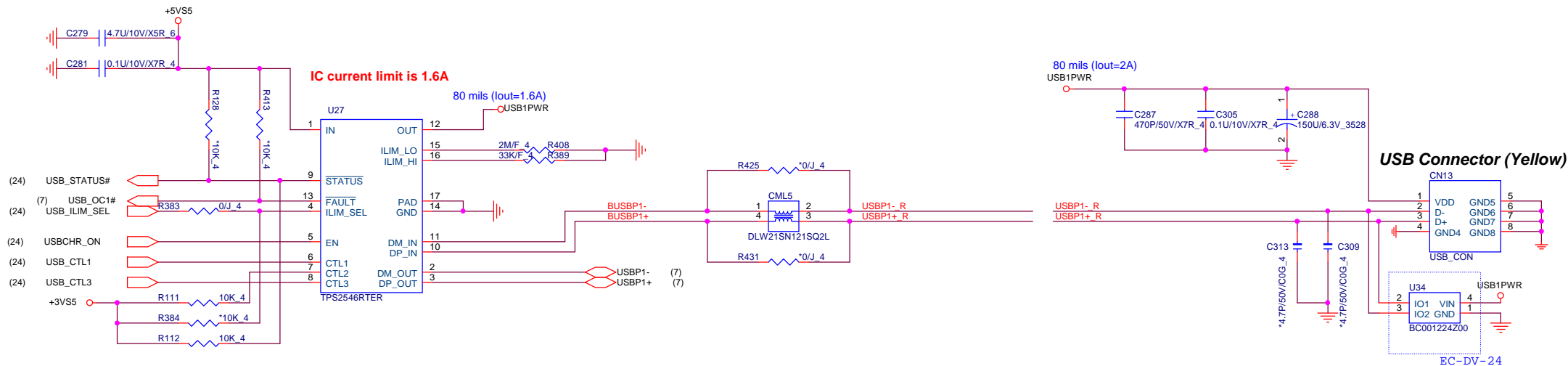
Pin 1	Pin 2	Pin 4
L	L	L
L	H	L
H	L	L
H	H	H

USB 2.0 Port *1

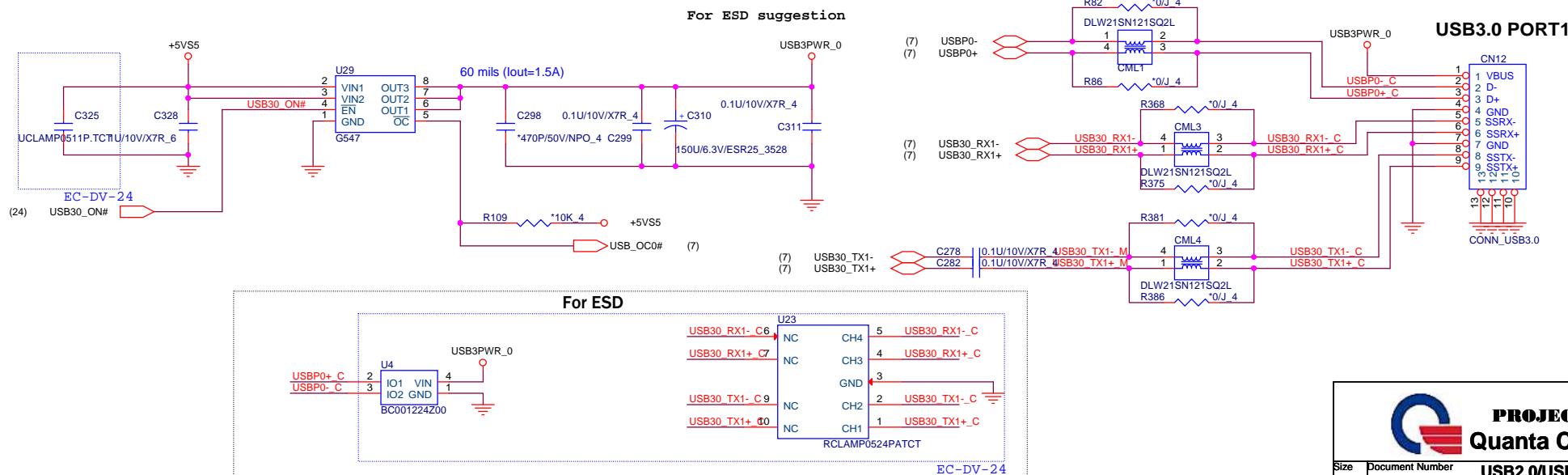
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(2,9,12,13,19,20,22,29,31,33,34,35,36,37,38)
(9,11,12,13,14,15,16,18,19,20,21,22,24,25,26,29,30,32,38,39)

+5VS5
+3VS5
+3.3V

23

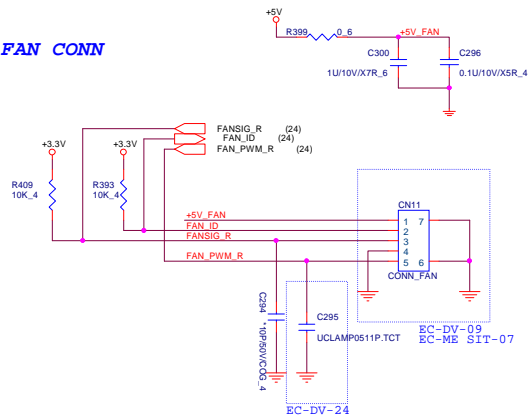
USB2.0 Port1

USB 3.0 Port *1





FAN CONN



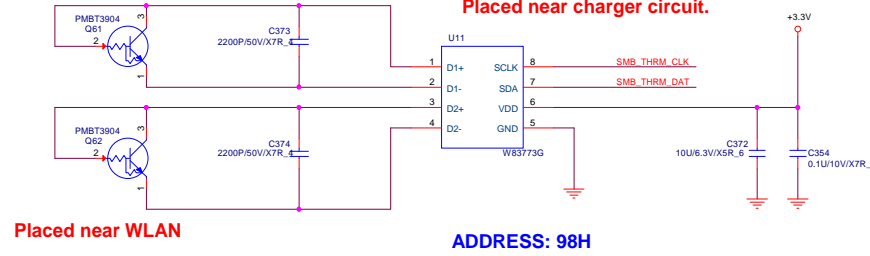
Thermal Sensor

EC-DV-11

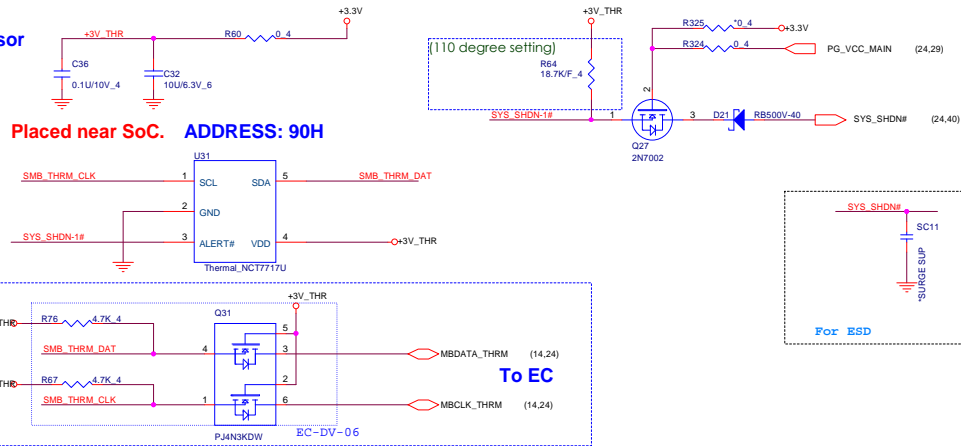
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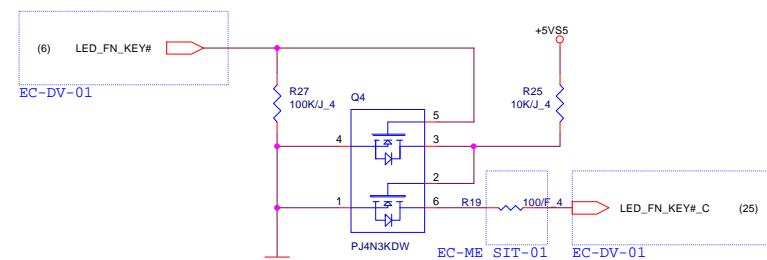
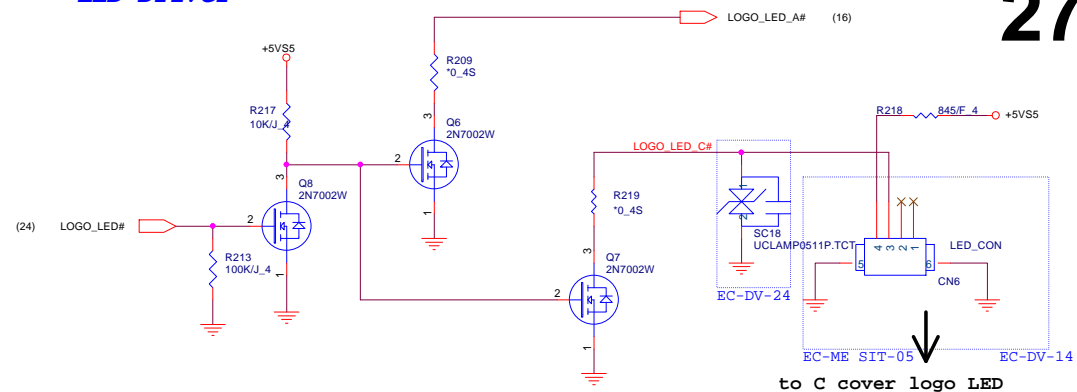
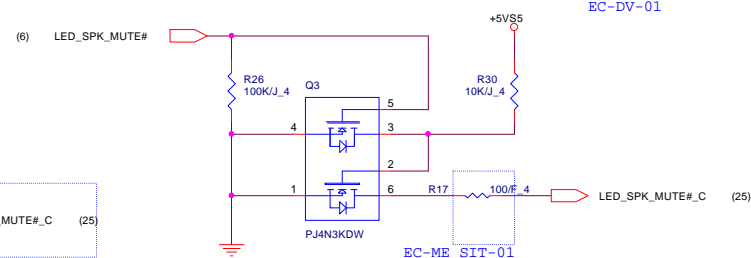
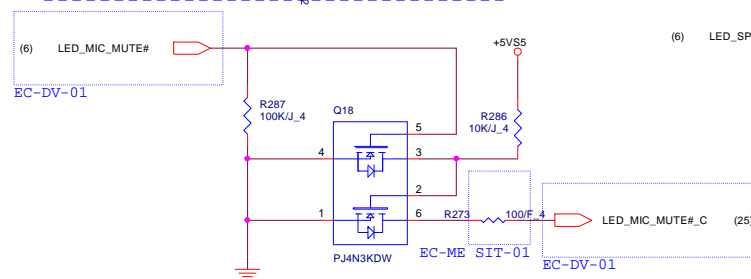
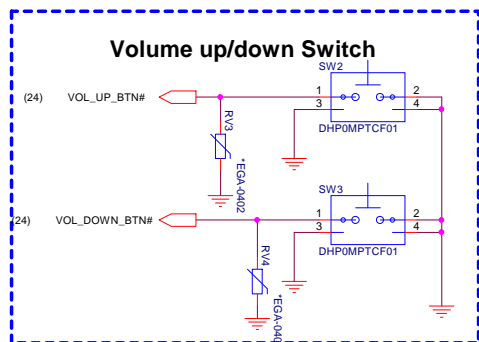
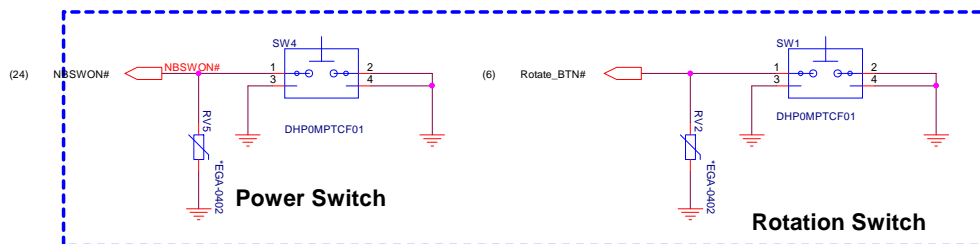
NOTE:
Place near IC Pin

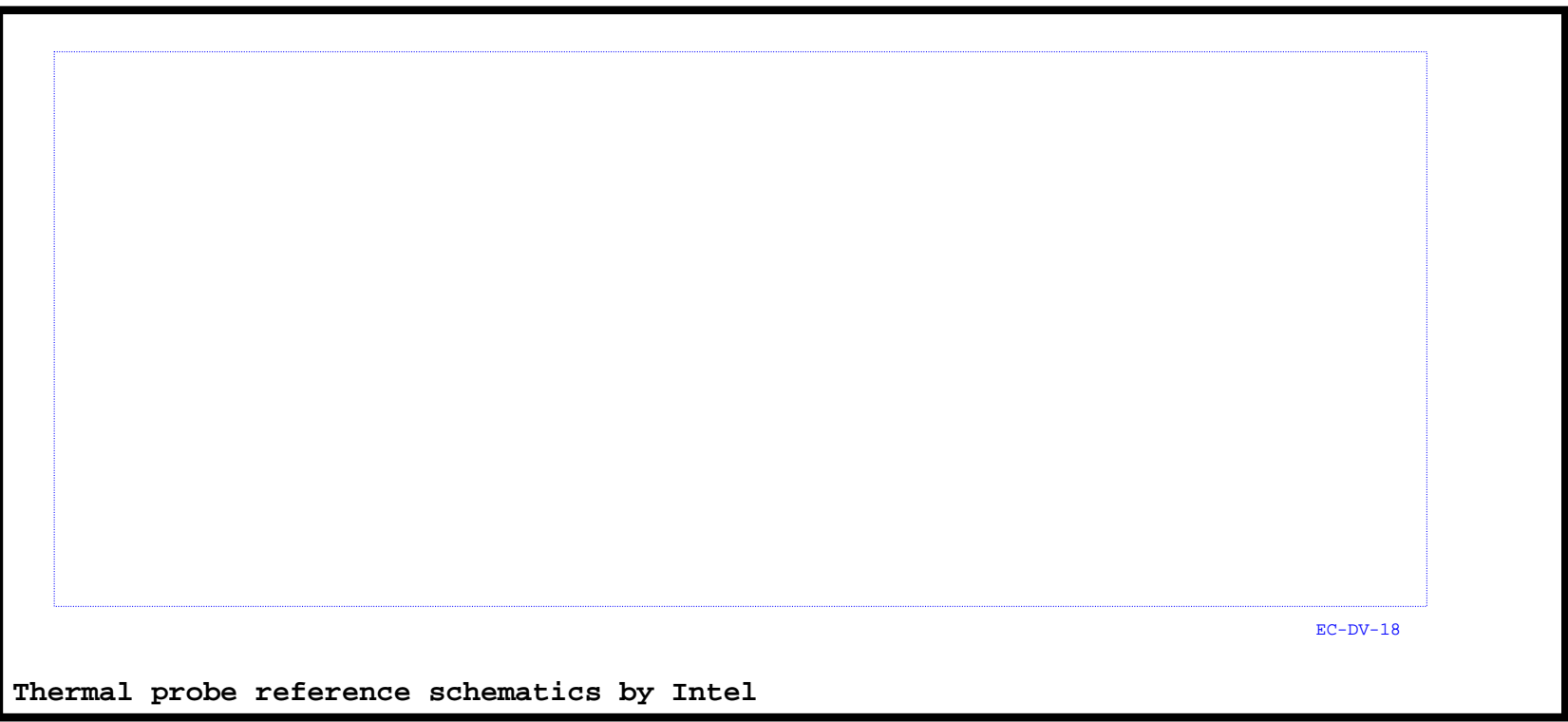
Placed near charger circuit.



CPU Thermal Sensor







Thermal probe reference schematics by Intel

EC-DV-18

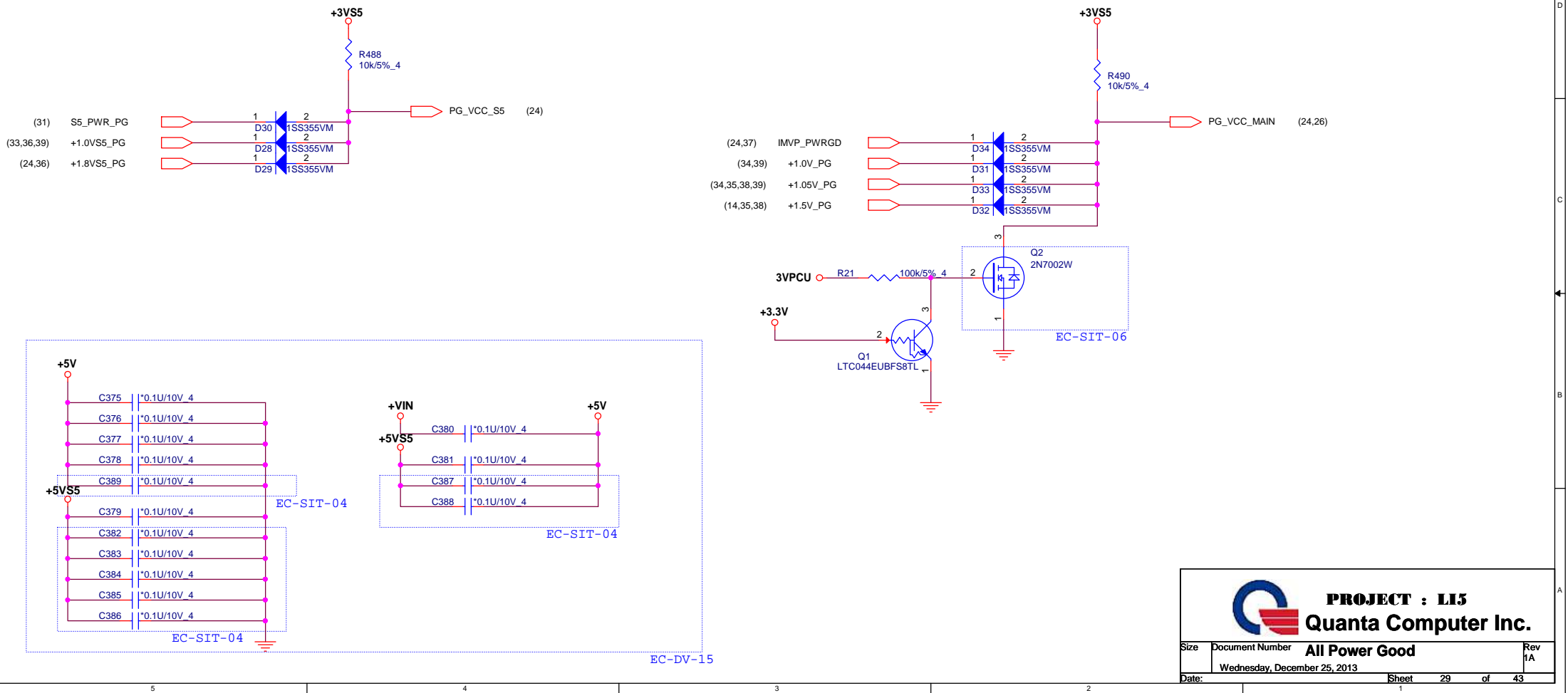


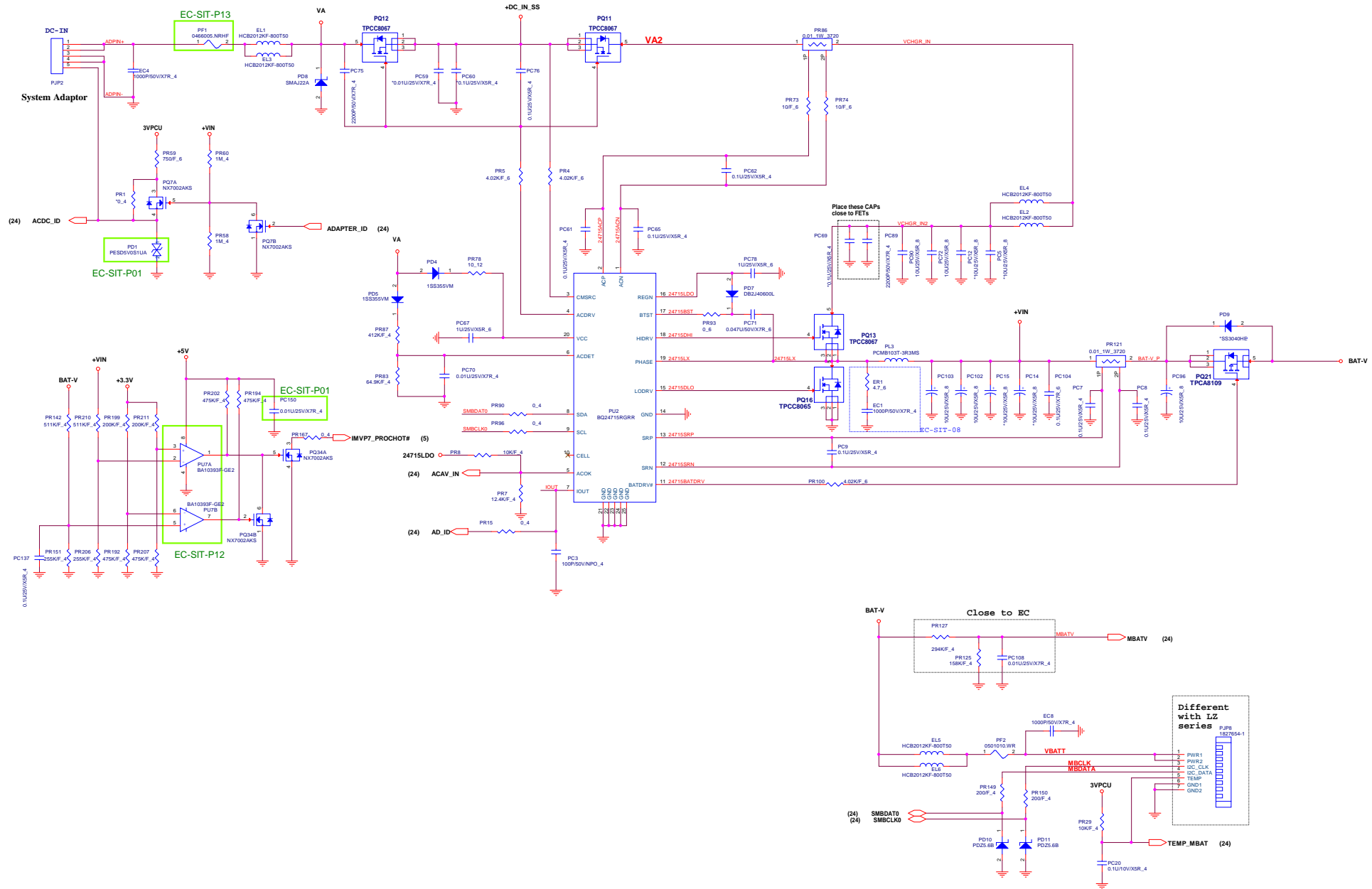
PROJECT : LI5
Quanta Computer Inc.

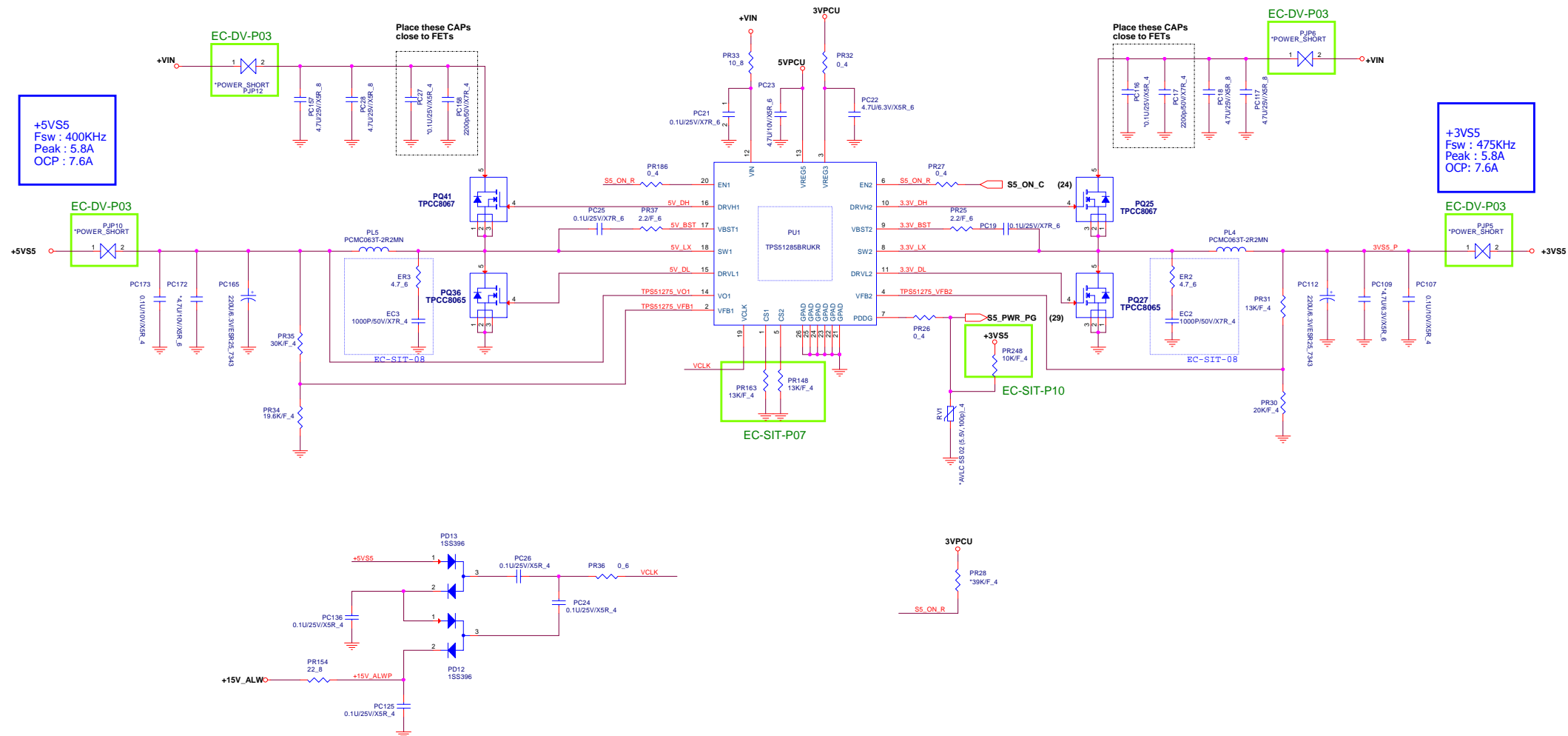
Size	Document Number	Rev
	AUDIO/SPK/HP/MIC	1A
Date:	Wednesday, December 25, 2013	Sheet 28 of 43

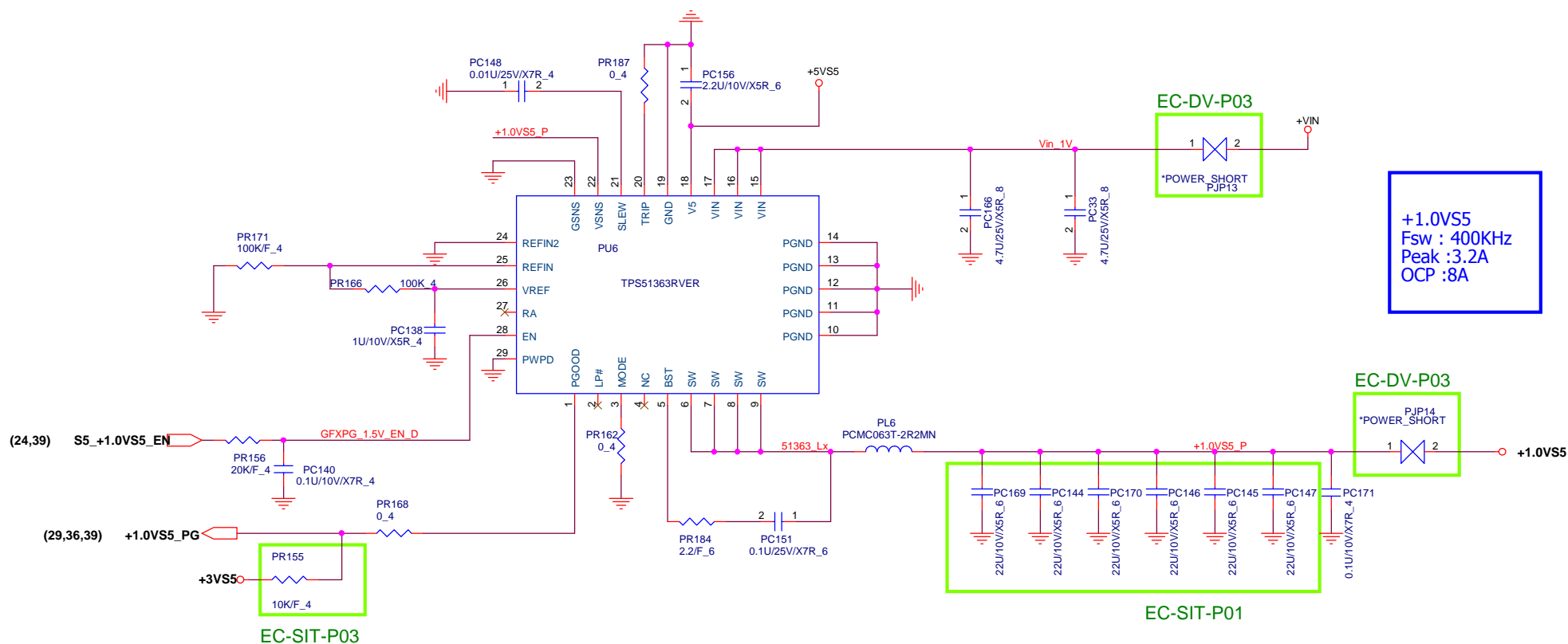
ALWAYS POWER GOOD

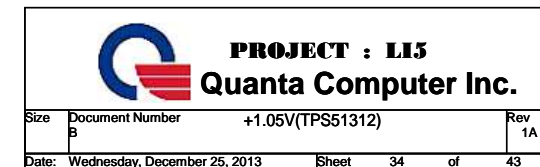
ALL SYSTEM POWER GOOD

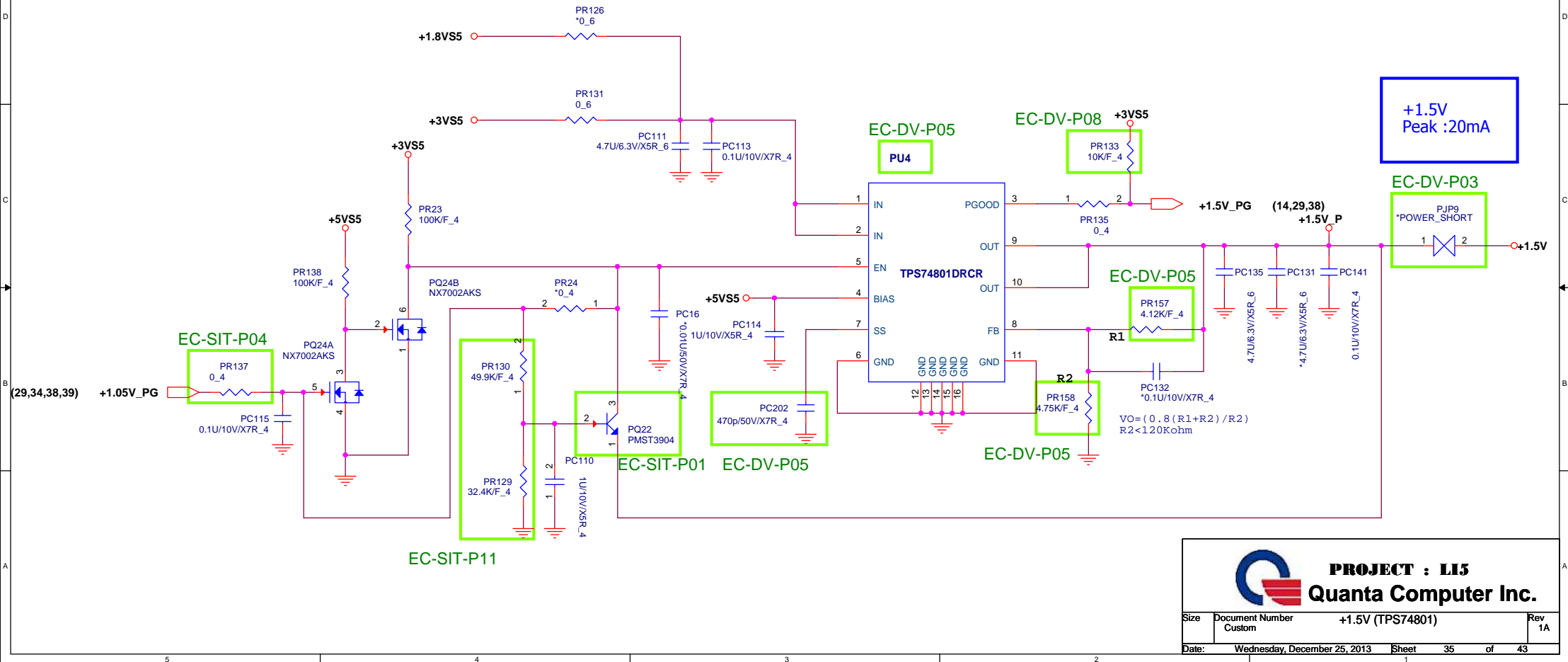






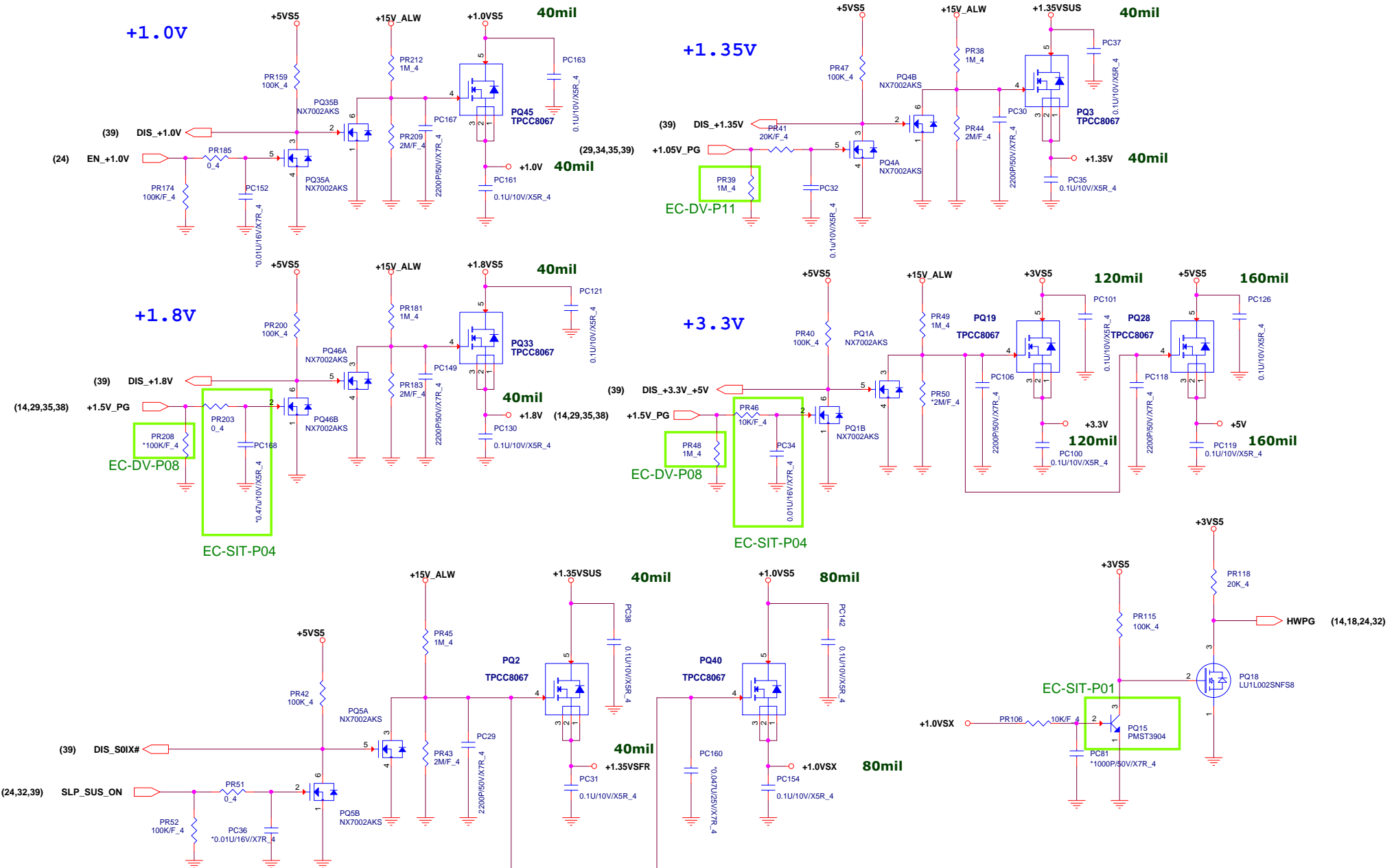






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Date:	Wednesday, December 25, 2013	Sheet	36 of 43

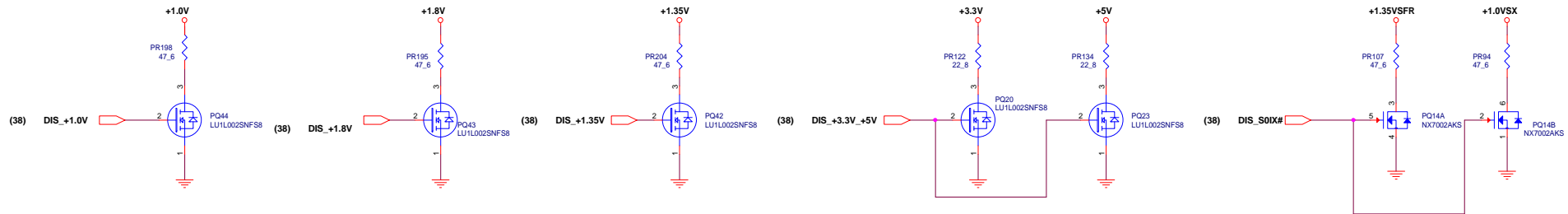
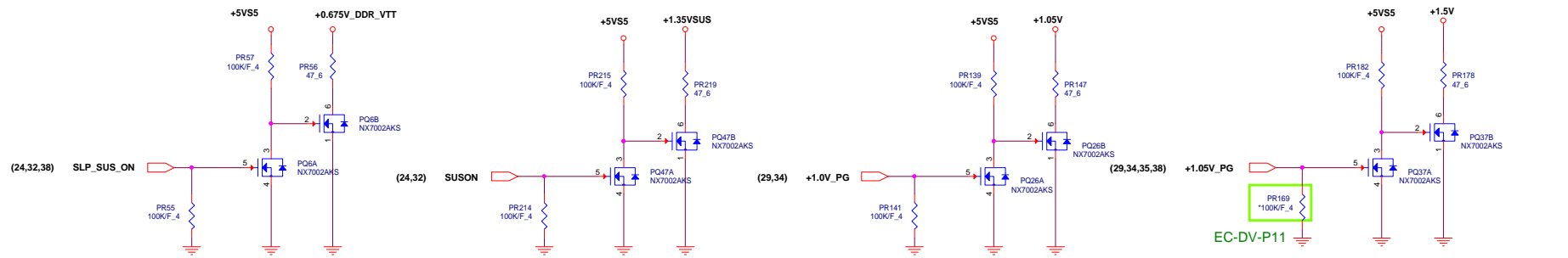
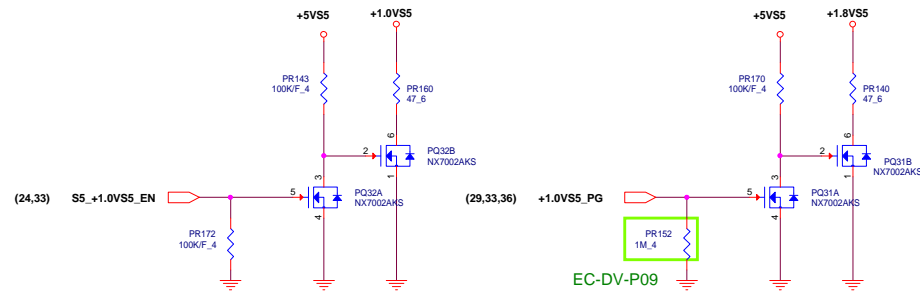
Load switch

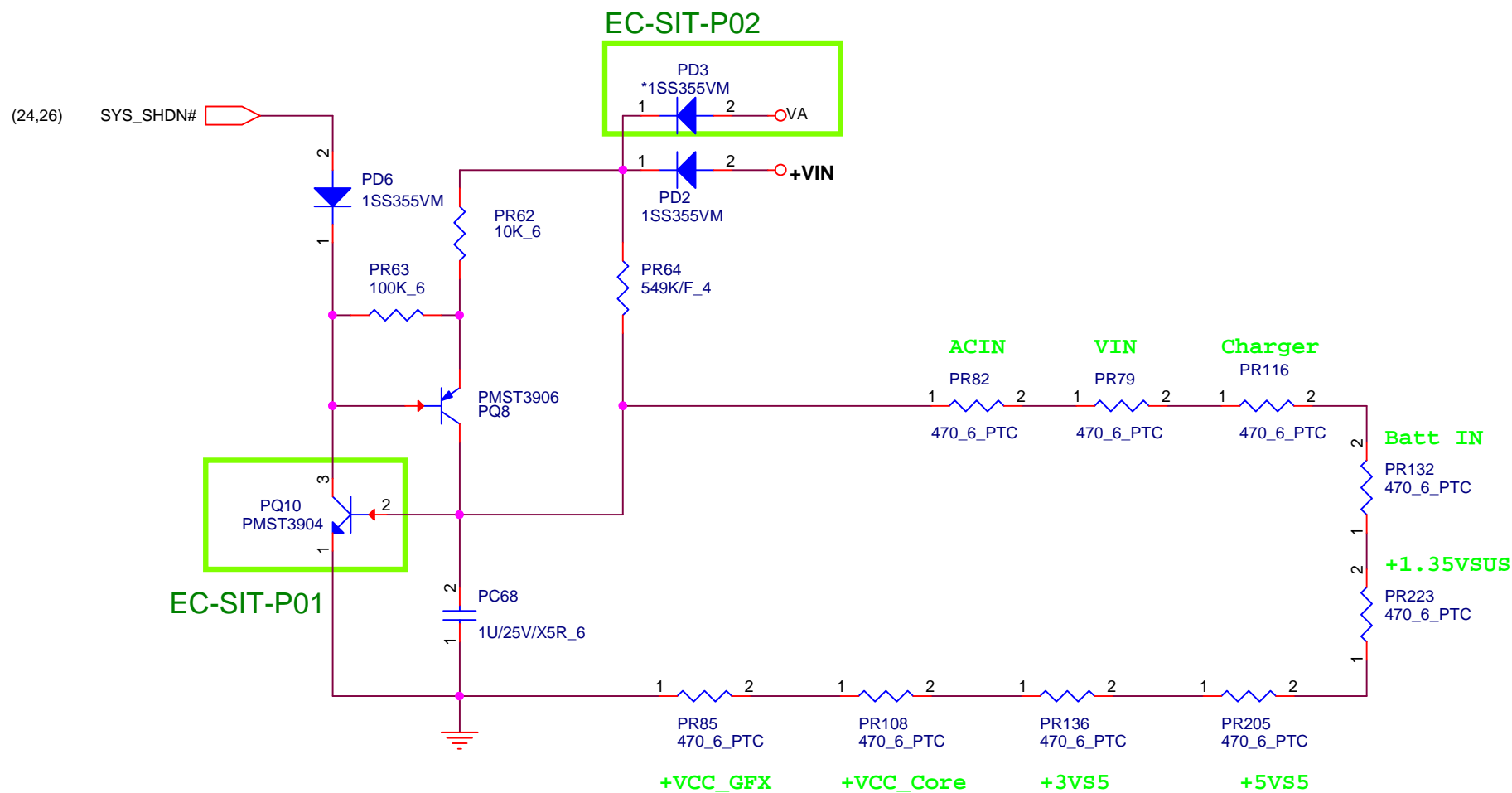


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Quanta Computer Inc.

Size	Document Number	Dis-charge IC (G5934RZ1U)	Rev
Custom			1A
Date:	Wednesday, December 25, 2013	Sheet	38 of 43

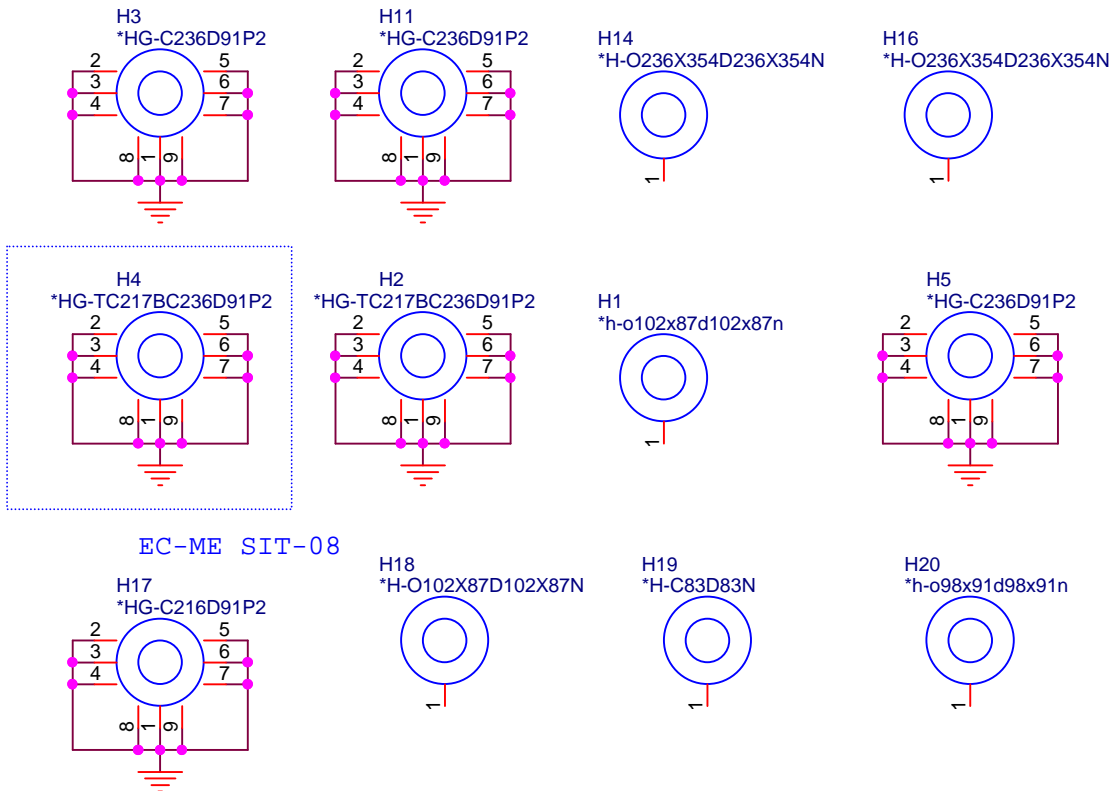
DISCHARGE





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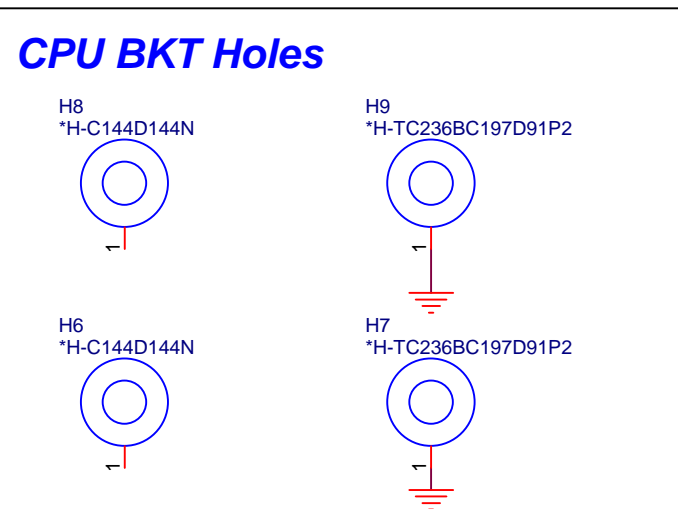
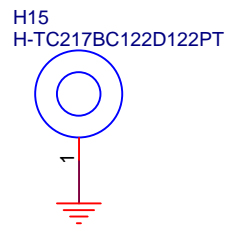
Size A	Document Number PTC	Rev 1A
Date: Wednesday, December 25, 2013	Sheet 40	of 43




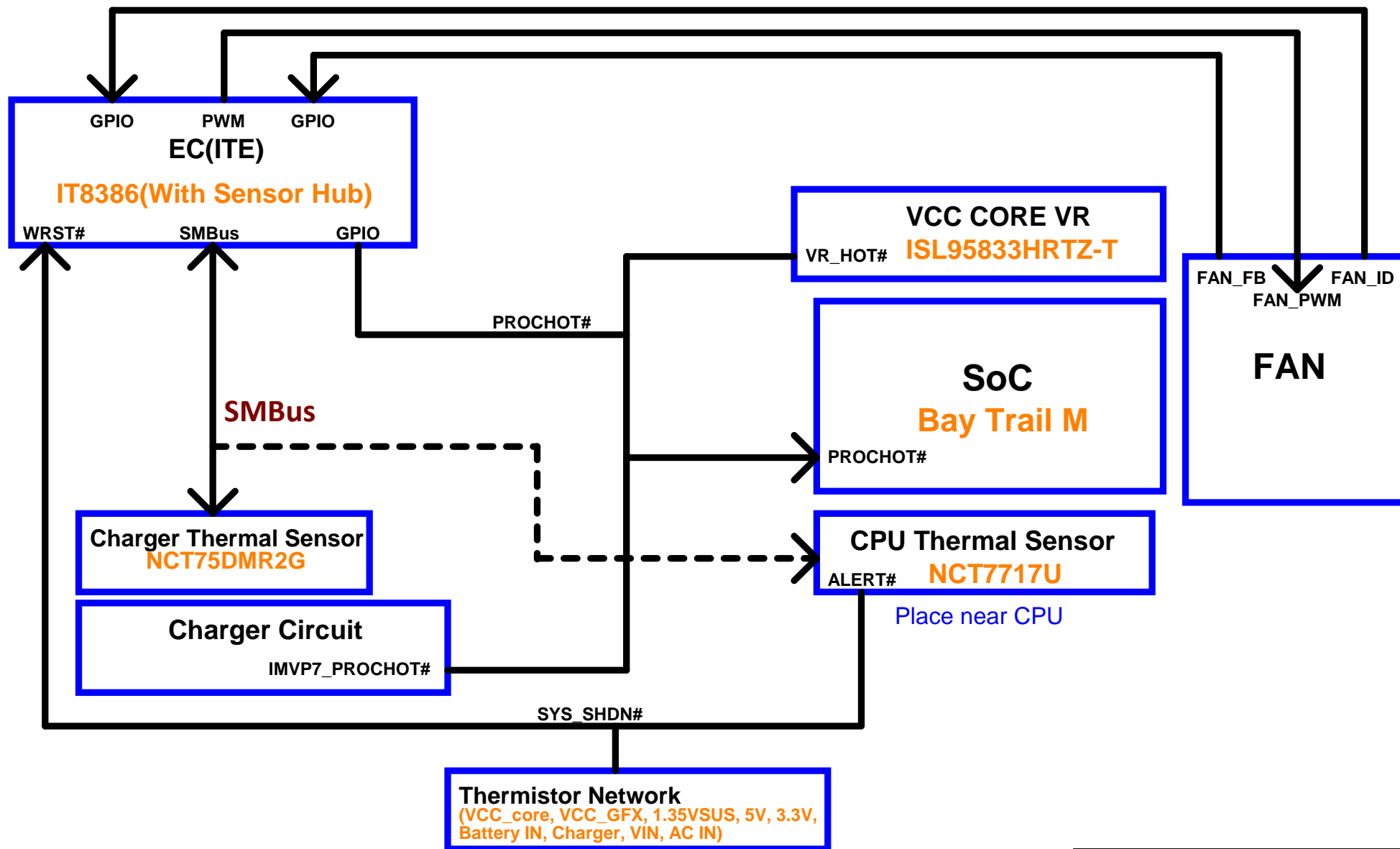
EC-ME SIT-08

Stuff NUT Location:

NGFF WLAN Nut

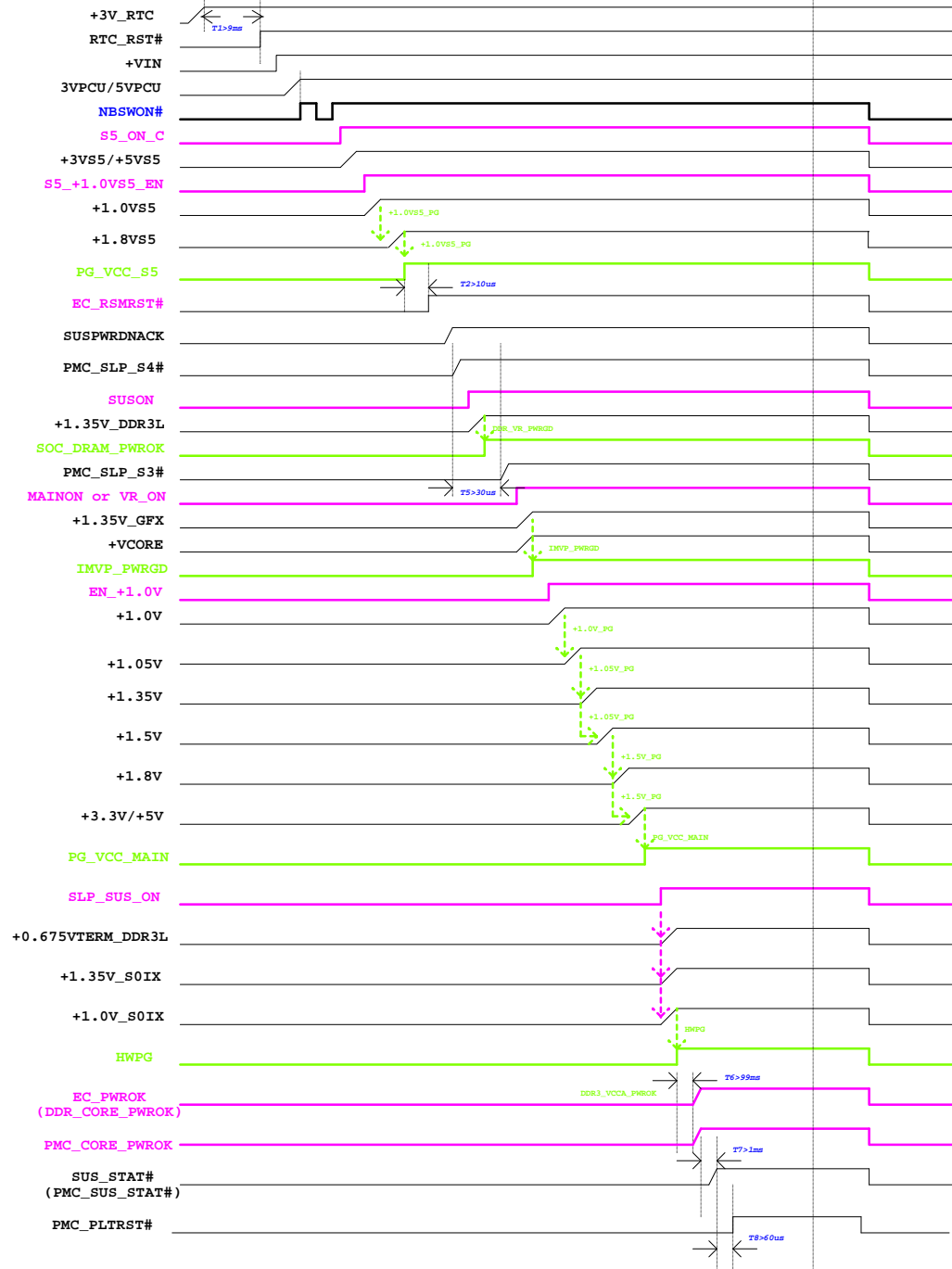


		PROJECT : LI5 Quanta Computer Inc.	
Size A	Document Number	Screw Hole	Rev 1A
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Power on sequence


OFF



Green signals (PG)
Blue timing -- Intel
Pink signals from EC

LI5 Schematic EC Tracking Record A to B version(SDV/FVT Planar)

EC #	Page	Description	Part Affected
EC-DV-01	6,25,27	Swap the net name.	net name only
EC-DV-02	24	Swap the net name.	net name only
EC-DV-03	25	Correct the net name(LID2#)	net name only
EC-DV-04	16	Add pull up resistor for SMBus of sensor board	R531,R532
EC-DV-05	21	Add pull down resistor for SSD DEVSLP pin.	R533
EC-DV-06	26	Parts stuff for thermal polling.	Q31,R76,R67
EC-DV-07	25	Change touch pad to I2C from SMB	net name only
EC-DV-08	16	Add MB ID for touch screen control board by different OS(Win8 or Chrome).	R535
EC-DV-09	16,21,26	Change PN and footprint for new connector	CN1,CN17,CN11,CN8
EC-DV-10	6,19	Separate the dual diode to single diode.	D35,D36,D37,D38
EC-DV-11	26	Change Thermal solution to Nuvoton to meet ECSL	U11,Q61,Q62,C372,C373,C374 Del: R223,R224,R231,R232,R236,R237,C354,U39,R513,R514,R515,R503, R504,R505,C329,U36,R479,R480,R481,R468,R469,R470
EC-DV-12	20	Change U18(transformer) footprint	U18
EC-DV-13	14	Correct the eDP AUX P/N net	net name only
EC-DV-14	27	Change LED connector CN6 footprint and PN	CN6
EC-DV-15	29	Reserve 0402 Cap for EMC requirement.	R375,R376,R377,R378,R379,R380,R381
EC-DV-16	24,16	Change LID_CLOSE Net connection, Connect to EC from hall sensor	R226
EC-DV-17	20	Stuff for 1G LAN transformer path. Unstuff C42 for 1.0V regulator mode	R93,R99,R359,R362,R367,R374,C42
EC-DV-18	19, 28	Delete thermal probe for PS2/SMB for touch pad reserved	R127,C99,R129,R125,R126,Q41,R429,R417,C85,Q37,Q40,Q39,Q38,U5, R423,C88,C89,C86,C87 R136,R135,R193,R190,R195,R156,R160,R197,R155,R322,R351 R538,R539,R540,R541,R542,R543,R544
EC-DV-19	25	Delete R198 combine the +3V_TCH_PAD to +3.3V	Delete R198
EC-DV-20	13	Remove R414 to avoid the USB hub into test mode	R414
EC-DV-21	12,25,24	Modify CHG/ATTN interrupt signal connection, Add SENSOR_HUB_INT signal	R545,R546,R547,R548,R549,R550,R551,Q64
EC-DV-22	14	Unstuff 1.2V LDO if eDP converter.	unstuff U17,C234,C236,R307,C14,C242,R299,R36,R43
EC-DV-23	12,25	Change pull up resistor vaule 4.7K to 2.2K in I2C	R119,R138,R222,R246,R517,R518
EC-DV-24	20	Stuff ESD component	U2,U3,U34,U4,SU1,U23,U10,U12,U16,EC5,C325,C295,C343,C340,C355, C346,SC17,SC18
EC-DV-25	6	Stuff PU resistor of BIOS EEPROM chip select pin.	R502

 Quanta Computer Inc. PROJECT : LV3D	
Size	Document Number
EC RECORD DV	
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LI5 Schematic EC Tracking Record B to C version(ME SIT Planar)


[illegible]

Title <Title>			
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Custom	Doc		<Rev Code>
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LI5 Schematic EC Tracking Record C to D version(SIT Planar)

[illegible]

Title <Title>			
Size Custom	Document Number Doc#		Rev <Rev Code>
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 Quanta Computer Inc. PROJECT : LV3D	
Size	Document Number
EC RECORD DV	
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